

Model Name: B365M HD3

rev 1.0

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1151-A
05	CPU_LGA1151-B-DDR4
06	CPU_LGA1151-C
07	CPU_LGA1150-D
08	DDR4 CHANNEL A
09	DDR4 CHANNEL B
10	PCH_CLK BUFFER
11	PCH_DMI,USB,PCIE
12	PCH_MISC
13	PCH SATA,PCIE,SATA_EXPRESS
14	PCH PWR
15	PCH GND
16	ITE 8686 LPC IO
17	HWM
18	FAN CTRL--SIO
19	PCI EXPRESS*16 SLOT
20	PCI EXPRESS*4 & *1 SLOT
21	
22	DUAL BIOS
23	
24	
25	
26	
27	ISL95858 PWM-IRON
28	ISL95858 VCORE-IRON

SHEET

TITLE

29	ISL95858 VCCGT-IRON
30	VCCSA_VCCIO_VCCPLL
31	RT8237_DDR_BEAD
32	RT8068A_VPP
33	RT8237_PCH-BEAD
34	DISCRETE POWER
35	NCT3933
36	ATX POWER , A_-PROCHOT
37	KB_MS_USB
38	DVI CONN
39	RTD2168 - DP to VGA - IC
40	RTD2168 - DP to VGA - Conn
41	HDMI
42	REALTEK 8118G
43	USB30_LAN CONNECTOR-8118G
44	Realtek ALC887
45	REAR AUDIO JACK
46	ADUIO LED
47	F_USB30
48	F_USB
49	R_USB30
50	F_PANEL
51	M.2 X4 (Q)
52	
53	COM, LPT, TPM
54	EMI-ESD
55	POWER MAP
56	NTC MAP

Gigabyte Technology

Title			Cover Sheet	
Size	Document Number	B365M HD3		Rev
Custom				1.0
Date:	Wednesday, March 13, 2019	Sheet	1	of 56

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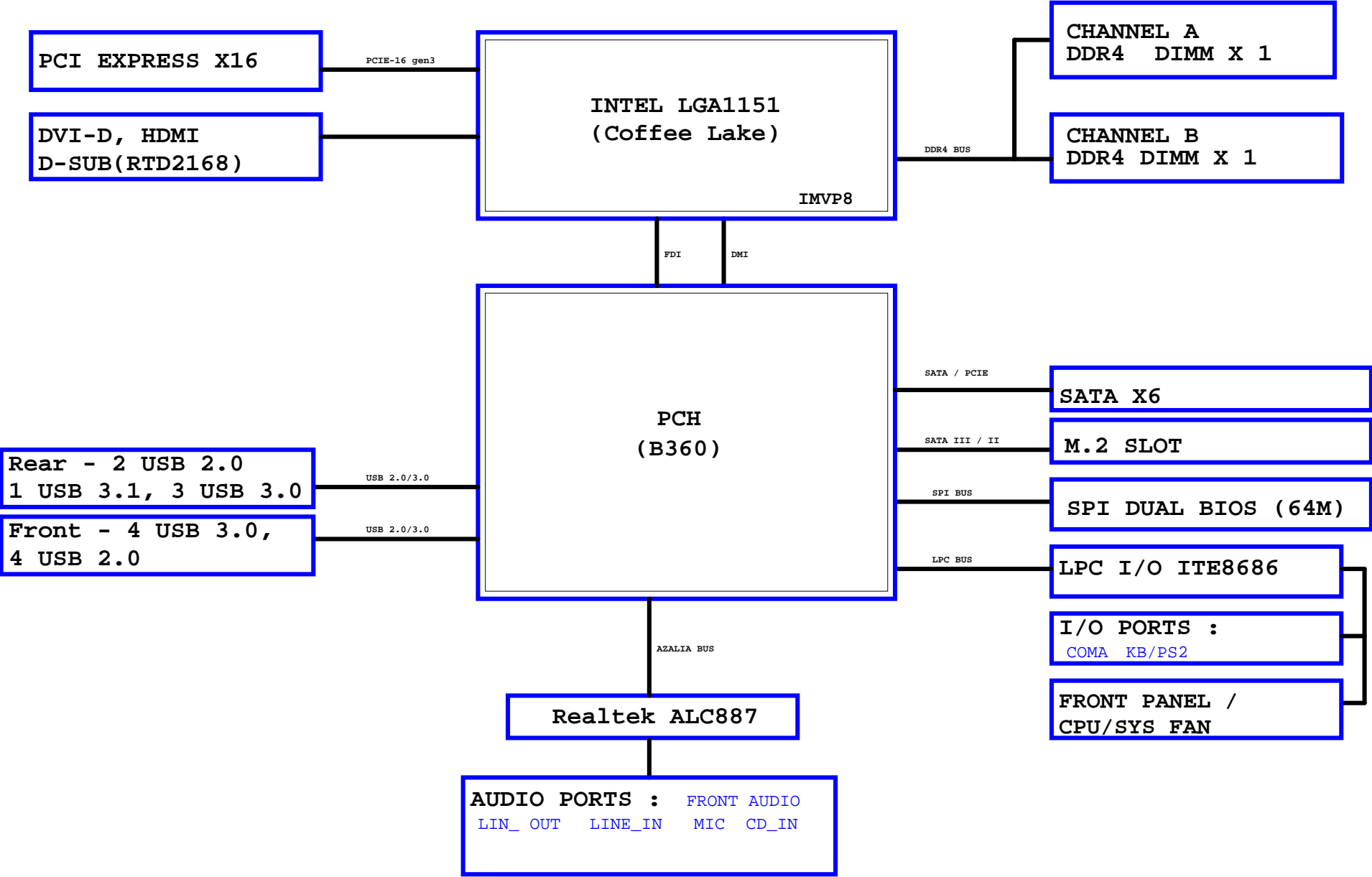
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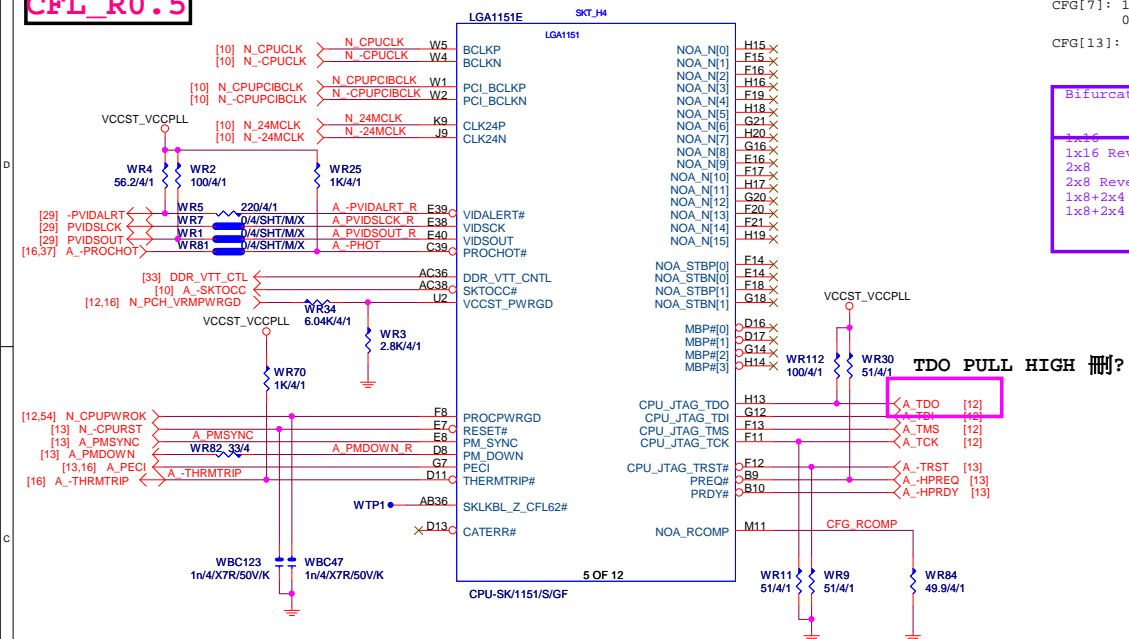
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BLOCK DIAGRAM



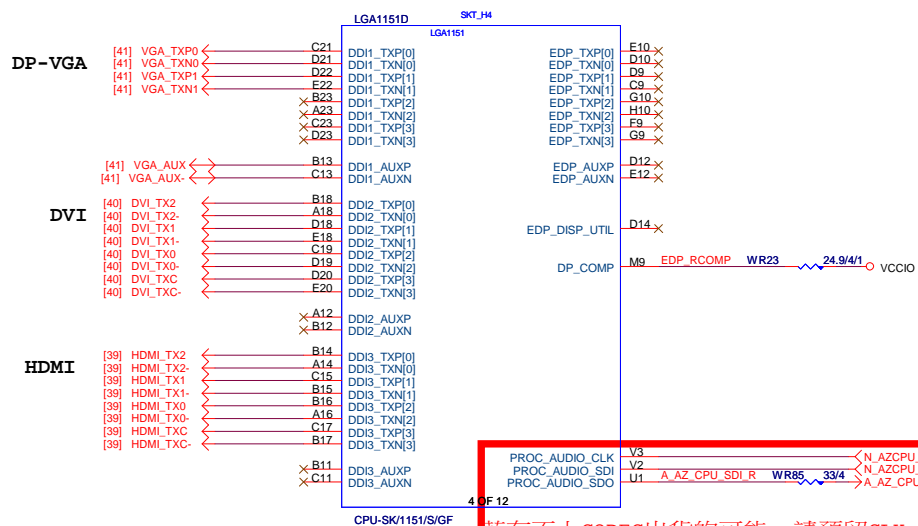
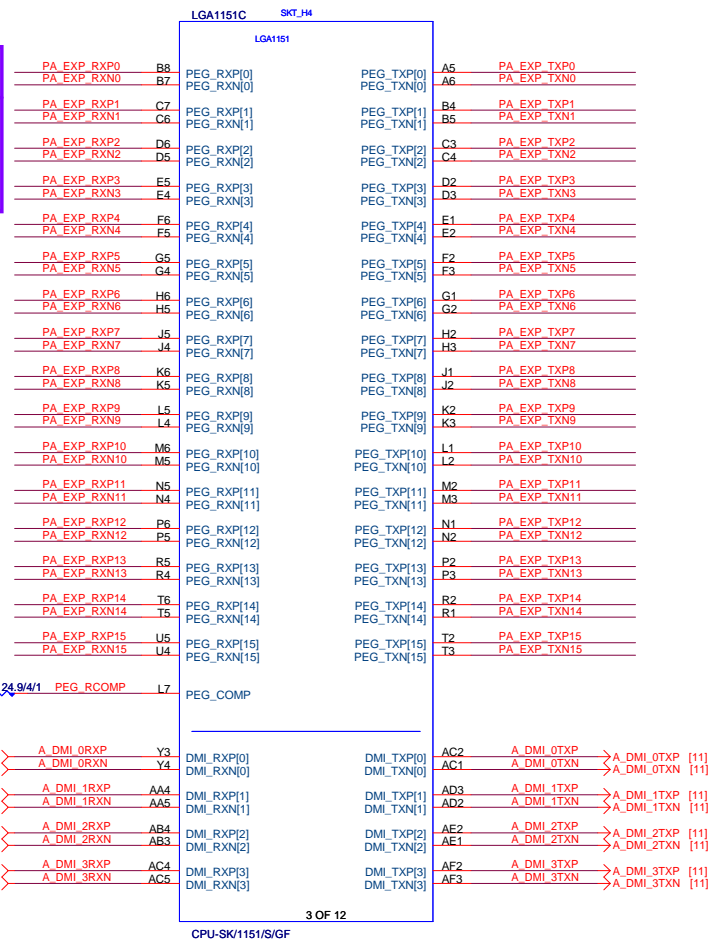
CFL_R0.5



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CFG[4]: 1=eDP enable / 0=eDP disable
CFG[7]: 1=PEG Train immediately following RESET
        0=PEG Wait for BIOS
```

CFG[13]: 1=VCCSA Fixed Mode / 0=SVID Mode

Bifurcation Config.	Signals Lanes		
	CFG[6]	CFG[5]	CFG[2]
1x16	1	1	1
1x16 Reversed	1	1	0
2x8	1	0	1
2x8 Reversed	1	0	0
1x8+2x4	0	0	1
1x8+2x4 Reversed	0	0	0



若有不上CODEC出貨的可能，請預留CLK and SDO對地電阻。

G-15u : (CPU-SK/1151/S/15)
10SC1-F01151-11R / 10SC1-F01151-12R
G-FL : (CPU-SK/1151/S/GF)
10SC1-F01151-21R / 10SC1-F01151-22R

LGA1151A SKT_H4

MDA0	AE38	DDR0_DQ[0]	DDR0_CK_P[0]	AW18	M_DCLKA0	M_DCLKA0 [8]
MDA1	AE37	DDR0_DQ[1]	DDR0_CK_N[0]	AV18	M_-DCLKA0	M_-DCLKA0 [8]
MDA2	AG38	DDR0_DQ[2]	DDR0_CK_P[1]	AW17	M_DCLKA1	M_DCLKA1 [8]
MDA3	AG37	DDR0_DQ[3]	DDR0_CK_N[1]	AY17	M_-DCLKA1	M_-DCLKA1 [8]
MDA4	AE39	DDR0_DQ[4]	DDR0_CK_P[2]	AW18		
MDA5	AE40	DDR0_DQ[5]	DDR0_CK_N[2]	AV18		
MDA6	AG39	DDR0_DQ[6]	DDR0_CK_P[3]	AT16		
MDA7	AG40	DDR0_DQ[7]	DDR0_CK_N[3]	AU16		
MDA8	AJ38	DDR0_DQ[8]				
MDA9	AJ37	DDR0_DQ[9]	DDR0_CKE[0]	AY24	CKEA0	CKEA0 [8]
MDA10	AL38	DDR0_DQ[10]	DDR0_CKE[1]	AW24	CKEA1	CKEA1 [8]
MDA11	AL37	DDR0_DQ[11]	DDR0_CKE[2]	AY24		
MDA12	AJ40	DDR0_DQ[12]	DDR0_CKE[3]	AV24		
MDA13	AJ39	DDR0_DQ[13]				
MDA14	AL39	DDR0_DQ[14]	DDR0_CS#0[0]	AW12	M_-CSA0	M_-CSA0 [8]
MDA15	AL40	DDR0_DQ[15]	DDR0_CS#1[0]	AU11	M_-CSA1	M_-CSA1 [8]
MDA16	AN38	DDR0_DQ[16]/DDR0_DQ[32]	DDR0_CS#2[0]	AV13		
MDA17	AR38	DDR0_DQ[17]/DDR0_DQ[33]	DDR0_CS#3[0]	AV19		
MDA18	AR37	DDR0_DQ[18]/DDR0_DQ[34]				
MDA19	AN39	DDR0_DQ[19]/DDR0_DQ[35]	DDR0_ODT[0]	AW11	MODT_A0	
MDA20	AN37	DDR0_DQ[20]/DDR0_DQ[36]	DDR0_ODT[1]	AU14	MODT_A1	
MDA21	AR37	DDR0_DQ[21]/DDR0_DQ[37]	DDR0_ODT[2]	AU12		
MDA22	AR38	DDR0_DQ[22]/DDR0_DQ[38]	DDR0_ODT[3]	AY19		
MDA23	AR40	DDR0_DQ[23]/DDR0_DQ[39]				
MDA24	AW37	DDR0_DQ[24]/DDR0_DQ[40]	DDR0_BA[0]/DDR0_CAB[4]/DDR0_BA[0]	AY13	SBA0A	SBA0A [8]
MDA25	AJ38	DDR0_DQ[25]/DDR0_DQ[41]	DDR0_BA[1]/DDR0_CAB[5]/DDR0_BA[1]	AV15	SBA1A	SBA1A [8]
MDA26	AV35	DDR0_DQ[26]/DDR0_DQ[42]	DDR0_BA[2]/DDR0_CAA[5]/DDR0_BG[0]	AW23	BG_A0	BG_A0 [8]
MDA27	AW35	DDR0_DQ[27]/DDR0_DQ[43]				
MDA28	AV37	DDR0_DQ[28]/DDR0_DQ[44]	DDR0_RAS#/DDR0_CAB[3]/DDR0_MA[16]	AW13	MAAA16	
MDA29	AV37	DDR0_DQ[29]/DDR0_DQ[45]	DDR0_WE#/DDR0_CAB[2]/DDR0_MA[14]	AV14	MAAA14	
MDA30	AT35	DDR0_DQ[30]/DDR0_DQ[46]	DDR0_CAS#/DDR0_CAB[1]/DDR0_MA[15]	AY11	MAAA15	
MDA31	AJ35	DDR0_DQ[31]/DDR0_DQ[47]				
MDA32	AN38	DDR0_DQ[32]/DDR1_DQ[0]	DDR0_MA[0]/DDR0_CAB[9]/DDR0_MA[0]	AW15	MAAA0	
MDA33	AN38	DDR0_DQ[33]/DDR1_DQ[1]	DDR0_MA[1]/DDR0_CAB[8]/DDR0_MA[1]	AU18	MAAA1	
MDA34	AV6	DDR0_DQ[34]/DDR1_DQ[2]	DDR0_MA[2]/DDR0_CAB[5]/DDR0_MA[2]	AU17	MAAA2	
MDA35	AU8	DDR0_DQ[35]/DDR1_DQ[3]	DDR0_MA[3]	AV19	MAAA3	
MDA36	AU8	DDR0_DQ[36]/DDR1_DQ[4]	DDR0_MA[4]	AT19	MAAA4	
MDA37	AV8	DDR0_DQ[37]/DDR1_DQ[5]	DDR0_MA[5]/DDR0_CAA[0]/DDR0_MA[5]	AU20	MAAA5	
MDA38	AV6	DDR0_DQ[38]/DDR1_DQ[6]	DDR0_MA[6]/DDR0_CAA[2]/DDR0_MA[6]	AV20	MAAA6	
MDA39	AV6	DDR0_DQ[39]/DDR1_DQ[7]	DDR0_MA[7]/DDR0_CAA[4]/DDR0_MA[7]	AU21	MAAA7	
MDA40	AY4	DDR0_DQ[40]/DDR1_DQ[8]	DDR0_MA[8]/DDR0_CAA[3]/DDR0_MA[8]	AT20	MAAA8	
MDA41	AV4	DDR0_DQ[41]/DDR1_DQ[9]	DDR0_MA[9]/DDR0_CAA[1]/DDR0_MA[9]	AT22	MAAA9	
MDA42	AT1	DDR0_DQ[42]/DDR1_DQ[10]	DDR0_MA[10]/DDR0_CAB[7]/DDR0_MA[10]	AY14	MAAA10	
MDA43	AT2	DDR0_DQ[43]/DDR1_DQ[11]	DDR0_MA[11]/DDR0_CAA[7]/DDR0_MA[11]	AU22	MAAA11	
MDA44	AV3	DDR0_DQ[44]/DDR1_DQ[12]	DDR0_MA[12]/DDR0_CAA[6]/DDR0_MA[12]	AV22	MAAA12	
MDA45	AV4	DDR0_DQ[45]/DDR1_DQ[13]	DDR0_MA[13]/DDR0_CAB[0]/DDR0_MA[13]	AV12	MAAA13	
MDA46	AT4	DDR0_DQ[46]/DDR1_DQ[14]	DDR0_MA[14]/DDR0_CAA[9]/DDR0_BG[1]	AV23	BG_A1	BG_A1 [8]
MDA47	AT3	DDR0_DQ[47]/DDR1_DQ[15]	DDR0_MA[15]/DDR0_CAA[8]/DDR0_ACT#	AU24		
MDA48	AP2	DDR0_DQ[48]/DDR1_DQ[32]				
MDA49	AP3	DDR0_DQ[49]/DDR1_DQ[33]	DDR0_PAR	AY15		
MDA50	AP3	DDR0_DQ[50]/DDR1_DQ[34]	DDR0_ALERT#	AT23		
MDA51	AM3	DDR0_DQ[51]/DDR1_DQ[35]				
MDA52	AP4	DDR0_DQ[52]/DDR1_DQ[36]				
MDA53	AM2	DDR0_DQ[53]/DDR1_DQ[37]	DDR0_DQSN[0]	AE38	M_DQSA0	
MDA54	AP1	DDR0_DQ[54]/DDR1_DQ[38]	DDR0_DQSN[1]	AK39	M_DQSA1	
MDA55	AM1	DDR0_DQ[55]/DDR1_DQ[39]	DDR0_DQSN[2]/DDR0_DQSN[4]	AP39	M_DQSA2	
MDA56	AK3	DDR0_DQ[56]/DDR1_DQ[40]	DDR0_DQSN[3]/DDR0_DQSN[5]	AU36	M_DQSA3	
MDA57	AH1	DDR0_DQ[57]/DDR1_DQ[41]	DDR0_DQSN[4]/DDR1_DQSN[0]	AW7	M_DQSA4	
MDA58	AK4	DDR0_DQ[58]/DDR1_DQ[42]	DDR0_DQSN[5]/DDR1_DQSN[1]	AU3	M_DQSA5	
MDA59	AH2	DDR0_DQ[59]/DDR1_DQ[43]	DDR0_DQSN[6]/DDR1_DQSN[4]	AN3	M_DQSA6	
MDA60	AH4	DDR0_DQ[60]/DDR1_DQ[44]	DDR0_DQSN[7]/DDR1_DQSN[5]	AJ3	M_DQSA7	
MDA61	AK2	DDR0_DQ[61]/DDR1_DQ[45]				
MDA62	AH3	DDR0_DQ[62]/DDR1_DQ[46]	DDR0_DQSP[0]	AE38	M_DQSA0	
MDA63	AK1	DDR0_DQ[63]/DDR1_DQ[47]	DDR0_DQSP[1]	AK38	M_DQSA1	
			DDR0_DQSP[2]	AP38	M_DQSA2	
			DDR0_DQSP[3]	AV36	M_DQSA3	
			DDR0_DQSP[4]	AV7	M_DQSA4	
			DDR0_DQSP[5]	AU2	M_DQSA5	
			DDR0_DQSP[6]	AN2	M_DQSA6	
			DDR0_DQSP[7]	AJ2	M_DQSA7	
			DDR0_DQSP[8]	AV32		
			DDR0_DQSN[8]	AU32		

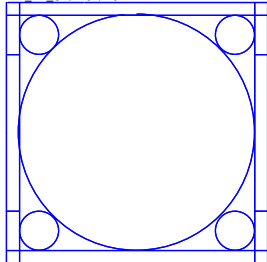
DDR CHANNEL A

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CPU-SK/1151/S/GF

CPU-SK/1151/S/GF

ILM_BP_CR/115X/NORMAL NI



Need check the new CPU-MB

LGA1151B SKT_H4

MDB0	AD34	DDR1_DQ[0]/DDR0_DQ[16]	DDR1_CK_P[0]	AM20	M_DCLKB0	M_DCLKB0 [9]
MDB1	AD35	DDR1_DQ[1]/DDR0_DQ[17]	DDR1_CK_N[0]	AM21	M_-DCLKB0	M_-DCLKB0 [9]
MDB2	AG35	DDR1_DQ[2]/DDR0_DQ[18]	DDR1_CK_P[1]	AP22	M_DCLKB1	M_DCLKB1 [9]
MDB3	AH35	DDR1_DQ[3]/DDR0_DQ[19]	DDR1_CK_N[1]	AP21	M_-DCLKB1	M_-DCLKB1 [9]
MDB4	AE35	DDR1_DQ[4]/DDR0_DQ[20]	DDR1_CK_P[2]	AN20		
MDB5	AE34	DDR1_DQ[5]/DDR0_DQ[21]	DDR1_CK_N[2]	AN21		
MDB6	AG34	DDR1_DQ[6]/DDR0_DQ[22]	DDR1_CK_P[3]	AN22		
MDB7	AH34	DDR1_DQ[7]/DDR0_DQ[23]	DDR1_CK_N[3]	AN23		
MDB8	AK35	DDR1_DQ[8]/DDR0_DQ[24]				
MDB9	AL35	DDR1_DQ[9]/DDR0_DQ[25]	DDR1_CKE[0]	AY29	CKEB0	CKEB0 [9]
MDB10	AL32	DDR1_DQ[10]/DDR0_DQ[26]	DDR1_CKE[1]	AV29	CKEB1	CKEB1 [9]
MDB11	AL32	DDR1_DQ[11]/DDR0_DQ[27]	DDR1_CKE[2]	AY29		
MDB12	AK34	DDR1_DQ[12]/DDR0_DQ[28]	DDR1_CKE[3]	AY29		
MDB13	AL34	DDR1_DQ[13]/DDR0_DQ[29]				
MDB14	AK31	DDR1_DQ[14]/DDR0_DQ[30]	DDR1_CS#0[0]	AP17	M_-CSB0	M_-CSB0 [9]
MDB15	AL31	DDR1_DQ[15]/DDR0_DQ[31]	DDR1_CS#1[0]	AN15	M_-CSB1	M_-CSB1 [9]
MDB16	AP35	DDR1_DQ[16]/DDR0_DQ[32]	DDR1_CS#2[0]	AN16		
MDB17	AN35	DDR1_DQ[17]/DDR0_DQ[33]	DDR1_CS#3[0]	AN17		
MDB18	AN32	DDR1_DQ[18]/DDR0_DQ[34]				
MDB19	AP32	DDR1_DQ[19]/DDR0_DQ[35]	DDR1_ODT[0]	AM16	MODT_B0	
MDB20	AN34	DDR1_DQ[20]/DDR0_DQ[36]	DDR1_ODT[1]	AL16	MODT_B1	
MDB21	AP34	DDR1_DQ[21]/DDR0_DQ[37]	DDR1_ODT[2]	AP15		
MDB22	AN31	DDR1_DQ[22]/DDR0_DQ[38]	DDR1_ODT[3]	AL15		
MDB23	AP31	DDR1_DQ[23]/DDR0_DQ[39]				
MDB24	AL29	DDR1_DQ[24]/DDR0_DQ[40]	DDR1_RAS#/DDR1_CAB[3]/DDR1_MA[16]	AN18	MAAB16	
MDB25	AM29	DDR1_DQ[25]/DDR0_DQ[41]	DDR1_WE#/DDR1_CAB[2]/DDR1_MA[14]	AL17	MAAB14	
MDB26	AP29	DDR1_DQ[26]/DDR0_DQ[42]	DDR1_CAS#/DDR1_CAB[1]/DDR1_MA[15]	AP16	MAAB15	
MDB27	AM28	DDR1_DQ[27]/DDR0_DQ[43]				
MDB28	AP12	DDR1_DQ[28]/DDR0_DQ[44]	DDR1_MA[0]/DDR1_CAB[9]/DDR1_MA[0]	AL18	SBA0A	SBA0A [9]
MDB29	AL28	DDR1_DQ[29]/DDR0_DQ[45]	DDR1_MA[1]/DDR1_CAB[8]/DDR1_MA[1]	AM18	SBA1A	SBA1A [9]
MDB30	AR28	DDR1_DQ[30]/DDR0_DQ[46]	DDR1_MA[2]/DDR1_CAB[5]/DDR1_MA[2]	AW28	BG_B0	BG_B0 [9]
MDB31	AP28	DDR1_DQ[31]/DDR0_DQ[47]	DDR1_MA[3]			
MDB32	AR12	DDR1_DQ[32]/DDR1_DQ[6]	DDR1_MA[4]	AL19	MAAB0	
MDB33	AP12	DDR1_DQ[33]/DDR1_DQ[7]	DDR1_MA[5]/DDR1_CAA[0]/DDR1_MA[5]	AL22	MAAB1	
MDB34	AM13	DDR1_DQ[34]/DDR1_DQ[18]	DDR1_MA[6]/DDR1_CAA[2]/DDR1_MA[6]	AM22	MAAB2	
MDB35	AL13	DDR1_DQ[35]/DDR1_DQ[19]	DDR1_MA[7]/DDR1_CAA[4]/DDR1_MA[7]	AM23	MAAB3	
MDB36	AR13	DDR1_DQ[36]/DDR1_DQ[20]	DDR1_MA[8]/DDR1_CAA[3]/DDR1_MA[8]	AP23	MAAB4	
MDB37	AP13	DDR1_DQ[37]/DDR1_DQ[21]	DDR1_MA[9]/DDR1_CAA[1]/DDR1_MA[9]	AL23	MAAB5	
MDB38	AM12	DDR1_DQ[38]/DDR1_DQ[22]	DDR1_MA[10]/DDR1_CAB[7]/DDR1_MA[10]	AV26	MAAB6	
MDB39	AL12	DDR1_DQ[39]/DDR1_DQ[23]	DDR1_MA[11]/DDR1_CAA[7]/DDR1_MA[11]	AW26	MAAB7	
MDB40	AP10	DDR1_DQ[40]/DDR1_DQ[24]	DDR1_MA[12]/DDR1_CAA[6]/DDR1_MA[12]	AU26	MAAB8	
MDB41	AR10	DDR1_DQ[41]/DDR1_DQ[25]	DDR1_MA[13]/DDR1_CAB[0]/DDR1_MA[13]	AW27	MAAB9	
MDB42	AR7	DDR1_DQ[42]/DDR1_DQ[26]	DDR1_MA[14]/DDR1_CAA[9]/DDR1_MA[14]	AP18	MAAB10	
MDB43	AP7	DDR1_DQ[43]/DDR1_DQ[27]	DDR1_MA[15]/DDR1_CAA[8]/DDR1_MA[15]	AU27	MAAB11	
MDB44	AR9	DDR1_DQ[44]/DDR1_DQ[28]	DDR1_MA[16]/DDR1_CAB[5]/DDR1_MA[16]	AV27	MAAB12	
MDB45	AP9	DDR1_DQ[45]/DDR1_DQ[29]	DDR1_MA[17]/DDR1_CAB[0]/DDR1_MA[17]	AR15	MAAB13	
MDB46	AR6	DDR1_DQ[46]/DDR1_DQ[30]	DDR1_MA[18]/DDR1_CAB[9]/DDR1_MA[18]	AY28	BG_B1	BG_B1 [9]
MDB47	AP6	DDR1_DQ[47]/DDR1_DQ[31]	DDR1_MA[19]/DDR1_CAA[8]/DDR1_MA[19]	AU28		
MDB48	AM10	DDR1_DQ[48]				
MDB49	AL10	DDR1_DQ[49]	DDR1_PAR	AL20		
MDB50	AM7	DDR1_DQ[50]	DDR1_ALERT#	AY25		
MDB51	AL7	DDR1_DQ[51]				
MDB52	AM9	DDR1_DQ[52]				
MDB53	AL9	DDR1_DQ[53]	DDR1_DQSN[0]/DDR0_DQSN[2]	AF34	M_DQSB0	
MDB54	AM6	DDR1_DQ[54]	DDR1_DQSN[1]/DDR0_DQSN[3]	AK33	M_DQSB1	
MDB55	AL6	DDR1_DQ[55]	DDR1_DQSN[2]/DDR0_DQSN[6]	AN33	M_DQSB2	
MDB56	AJ6	DDR1_DQ[56]	DDR1_DQSN[3]/DDR0_DQSN[5]	AN29	M_DQSB3	
MDB57	AJ7	DDR1_DQ[57]	DDR1_DQSN[4]/DDR1_DQSN[2]	AN13	M_DQSB4	
MDB58	AE6	DDR1_DQ[58]	DDR1_DQSN[5]/DDR1_DQSN[3]	AR8	M_DQSB5	
MDB59	AE7	DDR1_DQ[59]	DDR1_DQSN[6]	AM8	M_DQSB6	
MDB60	AH7	DDR1_DQ[60]	DDR1_DQSN[7]	AG6	M_DQSB7	
MDB61	AH6	DDR1_DQ[61]				
MDB62	AE7	DDR1_DQ[62]	DDR1_DQSP[0]	AF35	M_DQSB0	
MDB63	AE6	DDR1_DQ[63]	DDR1_DQSP[1]	AL33	M_DQSB1	
			DDR1_DQSP[2]	AP33	M_DQSB2	
			DDR1_DQSP[3]	AV28	M_DQSB3	
			DDR1_DQSP[4]	AN12	M_DQSB4	
			DDR1_DQSP[5]	AP8	M_DQSB5	
			DDR1_DQSP[6]	AL8	M_DQSB6	
			DDR1_DQSP[7]	AG7	M_DQSB7	
			DDR1_DQSP[8]	AN25		
			DDR1_DQSN[8]	AN26		

DDR CHANNEL B

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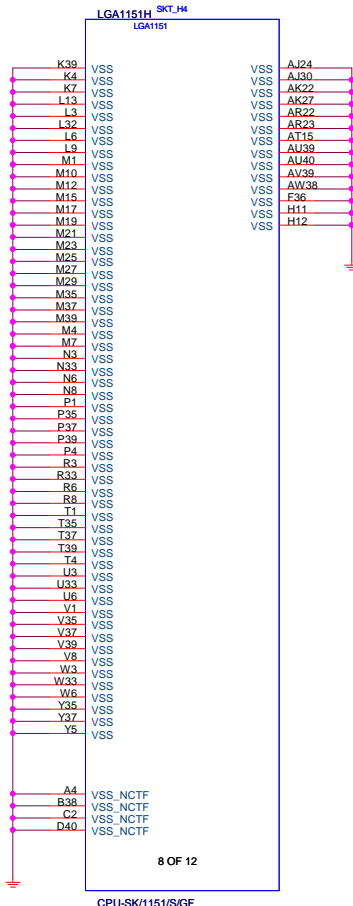
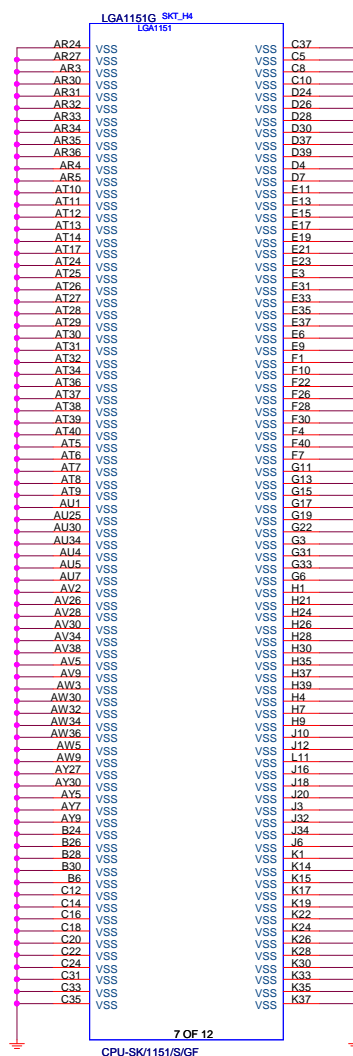
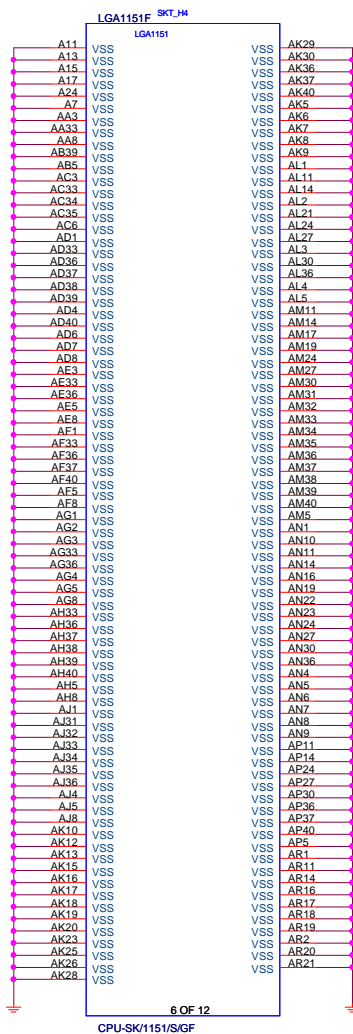
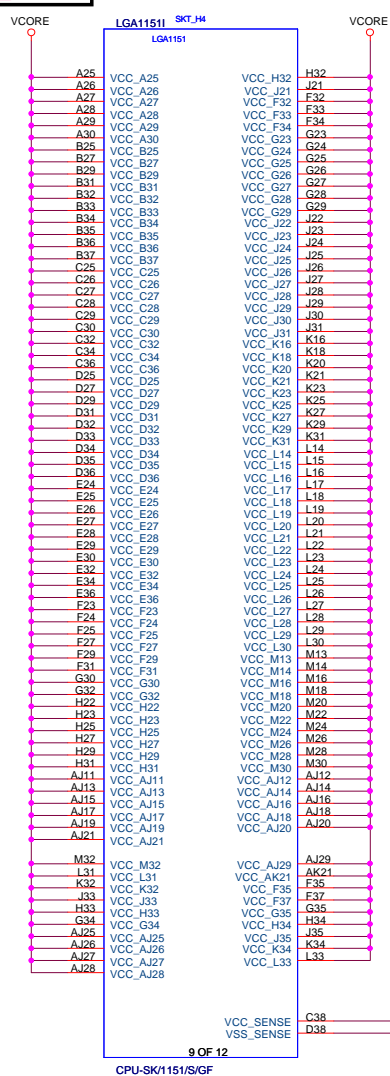
CPU-SK/1151/S/GF

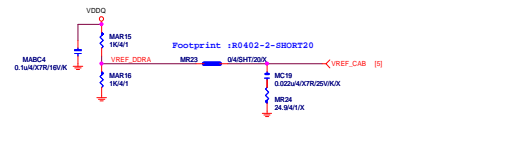
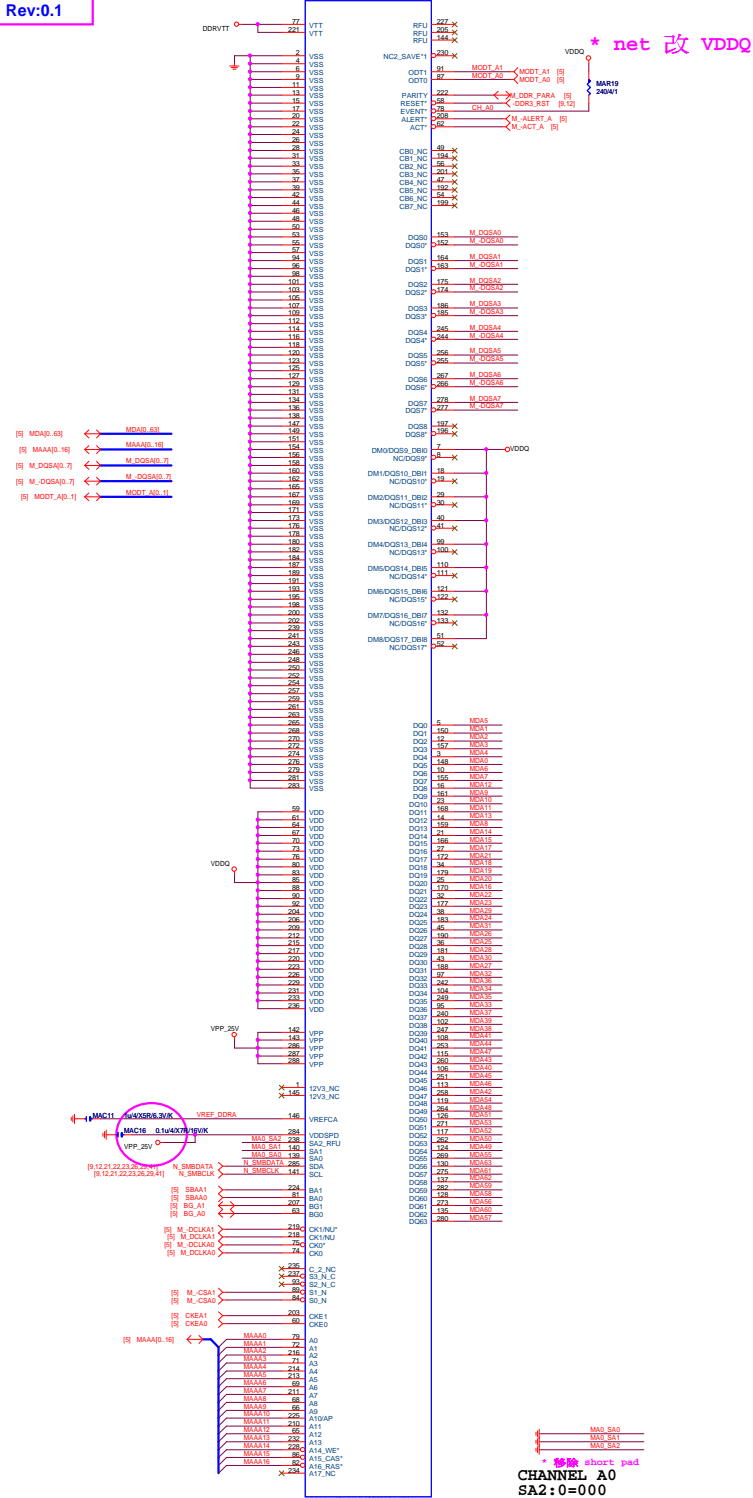
[8] MODT_A[0..1]	MODT_A[0..1]
[9] MODT_B[0..1]	MODT_B[0..1]
[8] MDA[0..63]	MDA[0..63]
[9] MDB[0..63]	MDB[0..63]
[8] M_DQSA[0..7]	M_DQSA[0..7]
[8] M_-DQSA[0..7]	M_-DQSA[0..7]
[9] MAAA[0..16]	MAAA[0..16]
[9] MAAB[0..16]	MAAB[0..16]
[9] M_DQSB[0..7]	M_DQSB[0..7]
[9] M_-DQSB[0..7]	M_-DQSB[0..7]

DDR_VREF_CA	AB40	VREF_CAB	VREF_CAB [9]
DDR0_VREF_DQ	AC40		
DDR1_VREF_DQ	AC39	VREF_DQB	VREF_DQB [9]

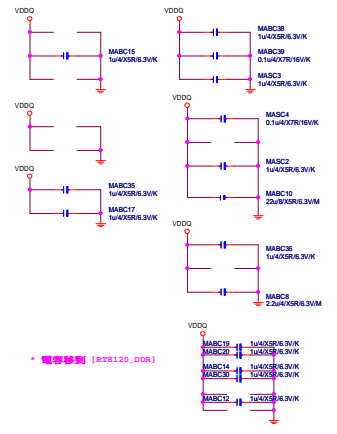
Gigabyte Technology

Title		CPU LGA1151-B		Rev	1.0
Size	Document Number				
Custom	B365M HD3				
Date:	Wednesday, April 17, 2019	Sheet	5	of	56



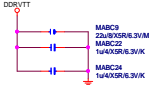


DDR12V Decouple



* 電容移測 [R19120_D0R]

DDRVTT Decouple

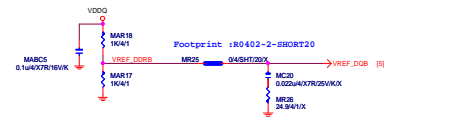
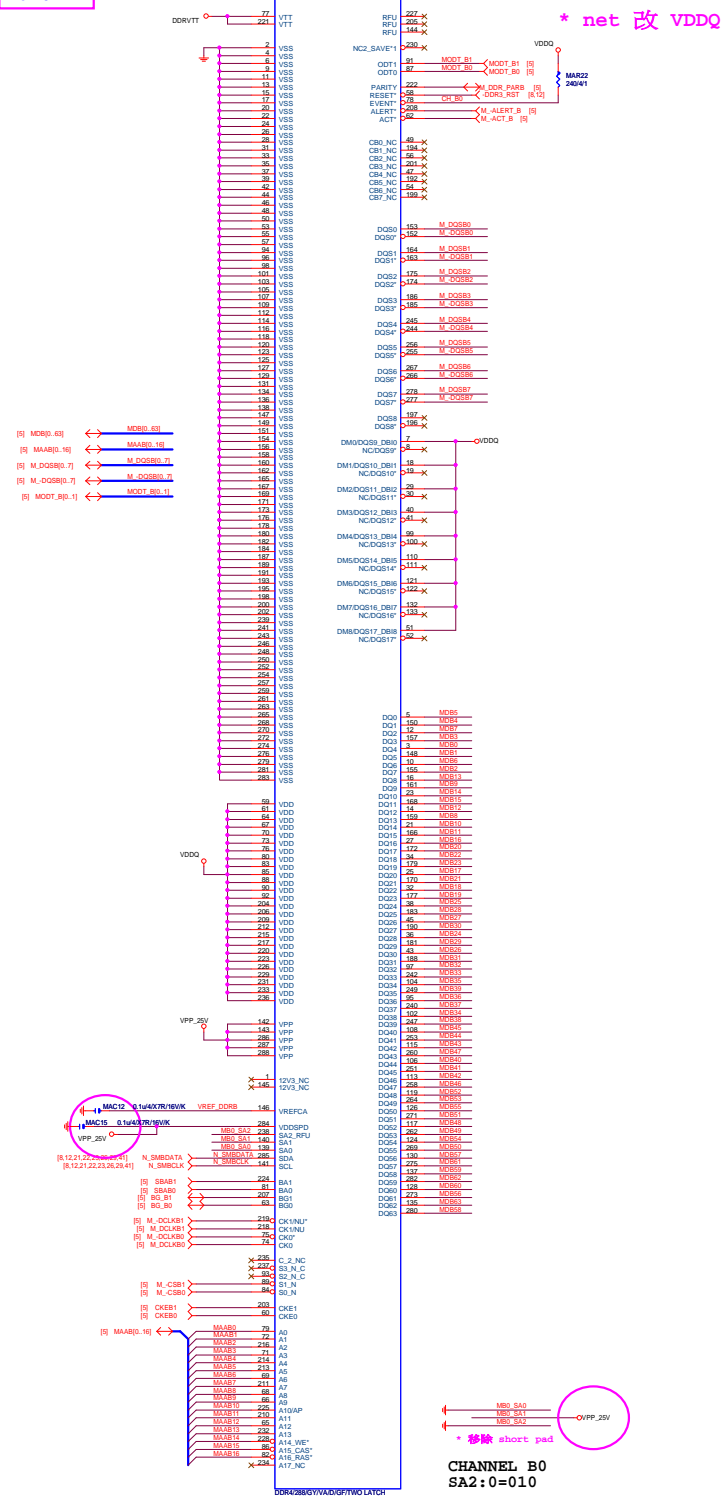


* 新電容

* 新電容

DDR4_1
00442889GWWLDFIWO LATCH
* 灰色 雙耳扣
CHANNEL A0
SA2:0=000

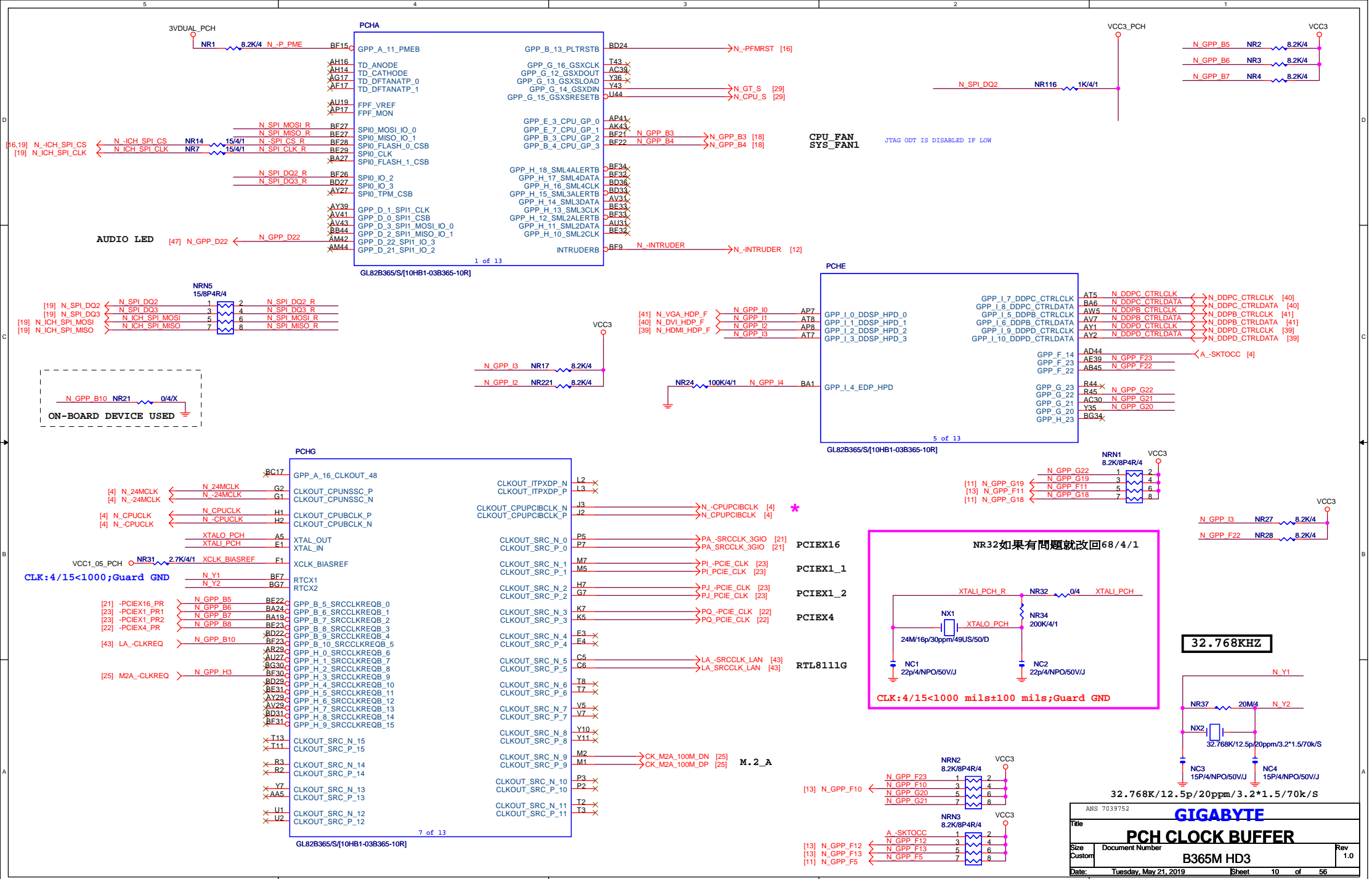
Rev:0.1

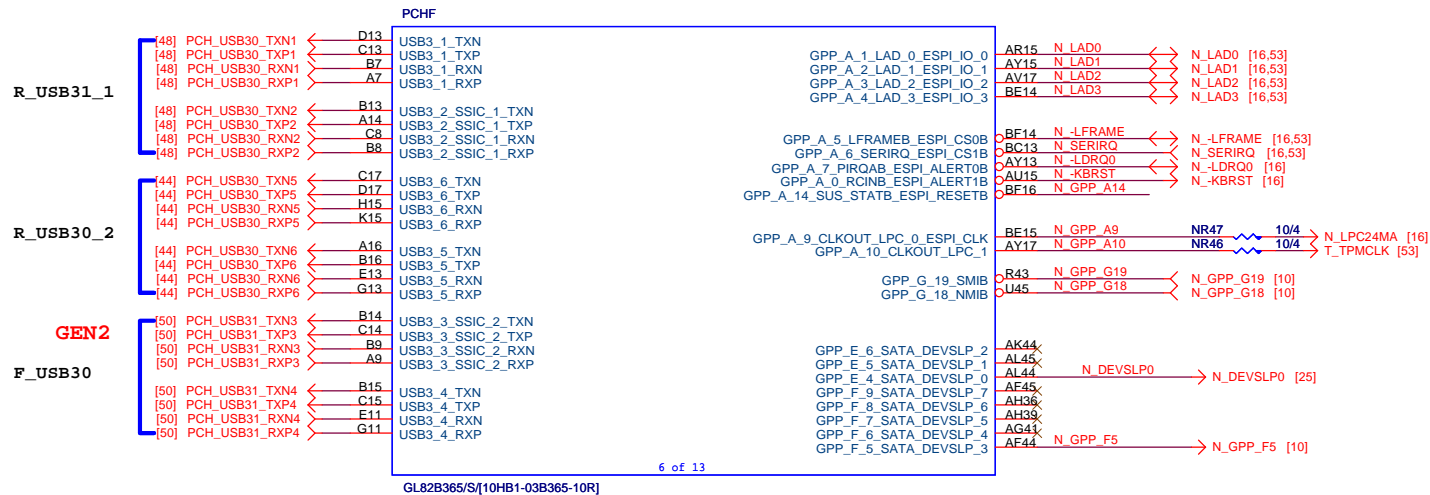
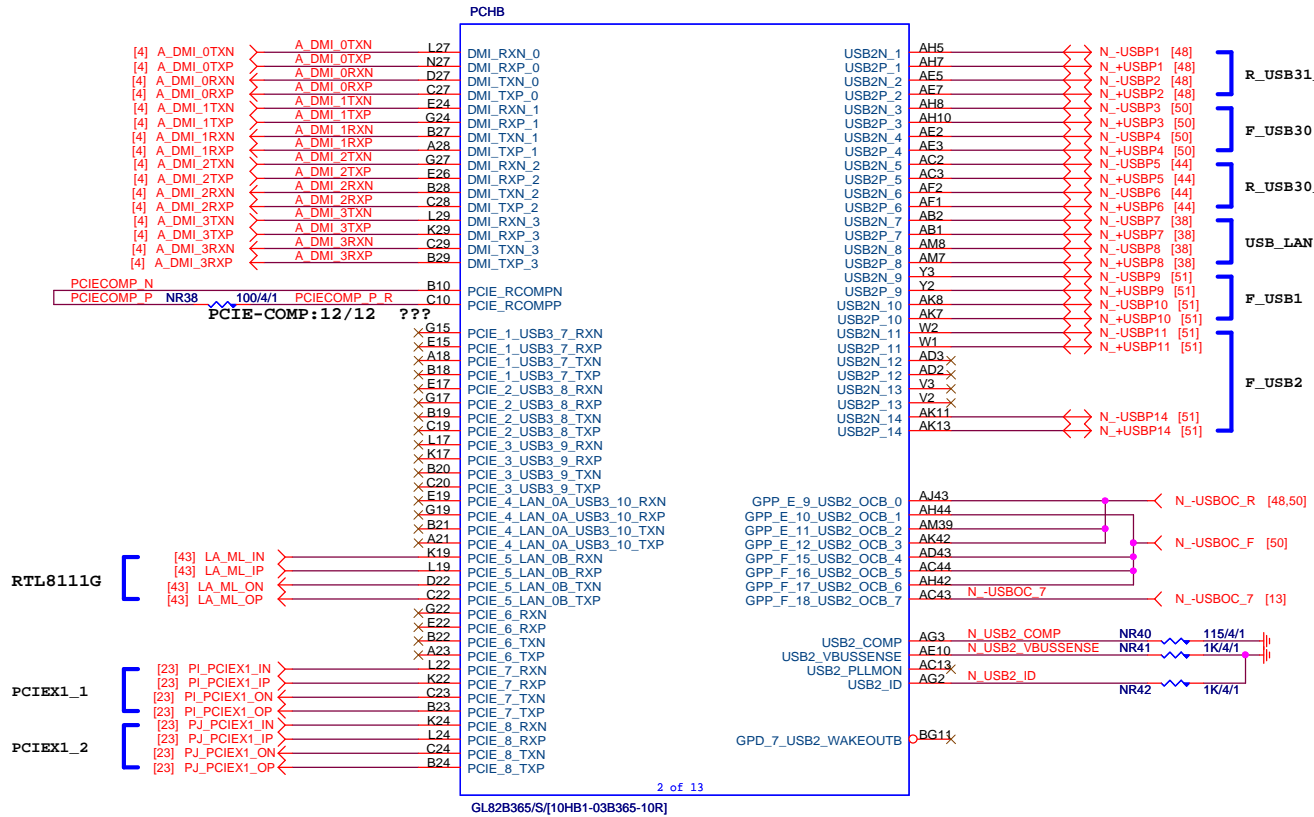


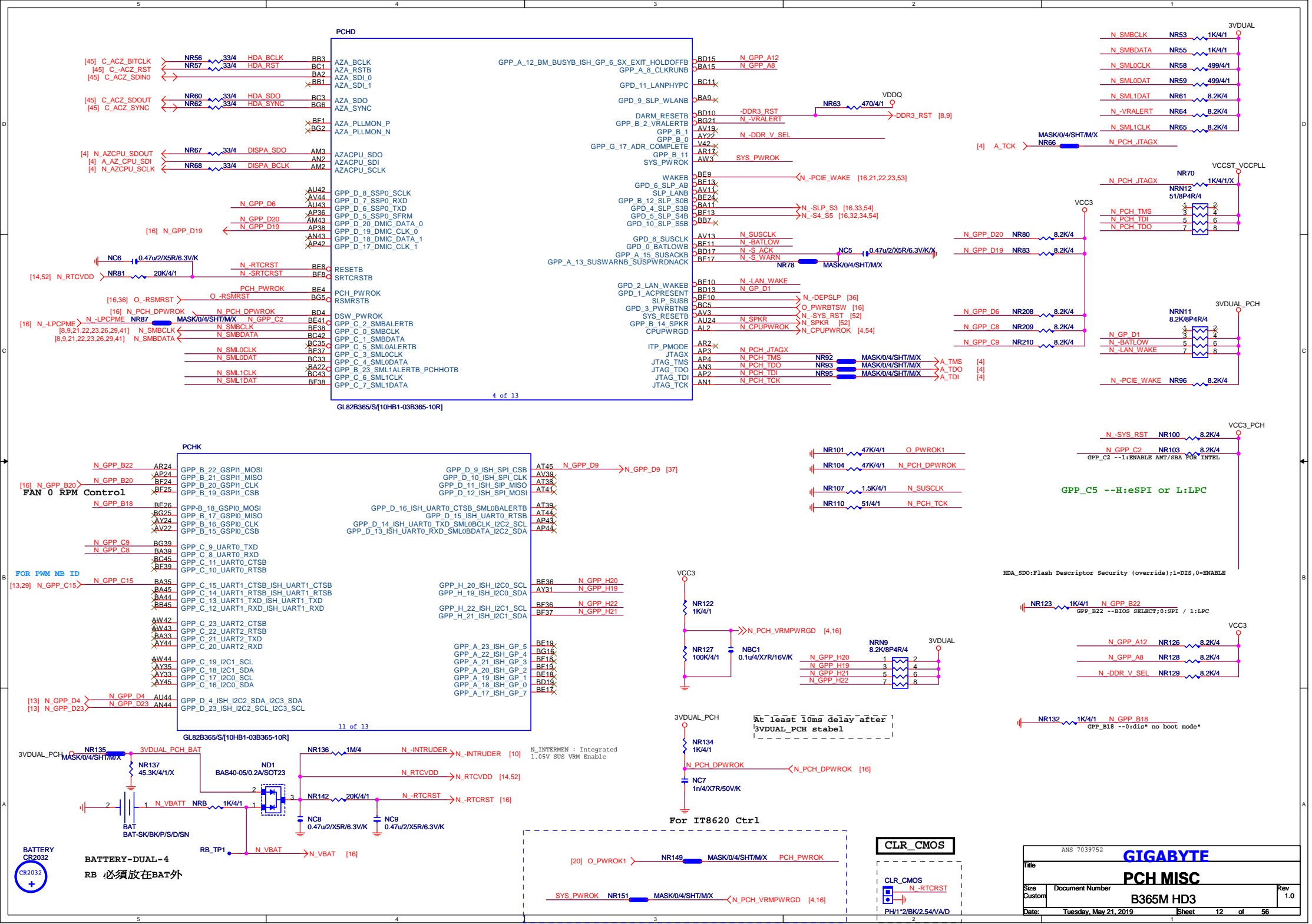
CHANNEL B0
SA2:0=010

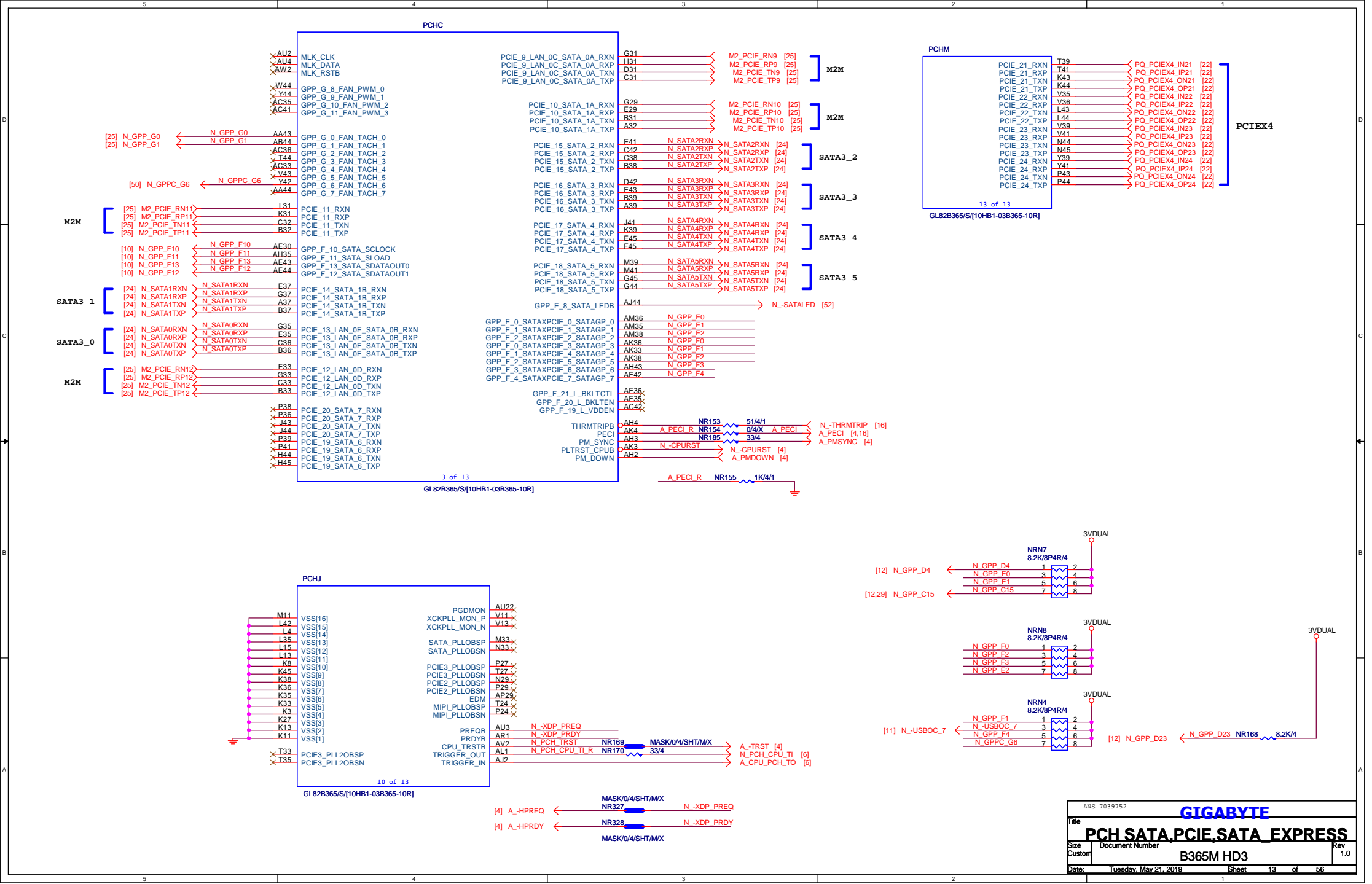
Gigabyte Technology

Title				DDR4 CHANNEL B			
Size	Document Number						Rev
Custom	B365M HD3						1.0
Date	Sheet		9		of		56







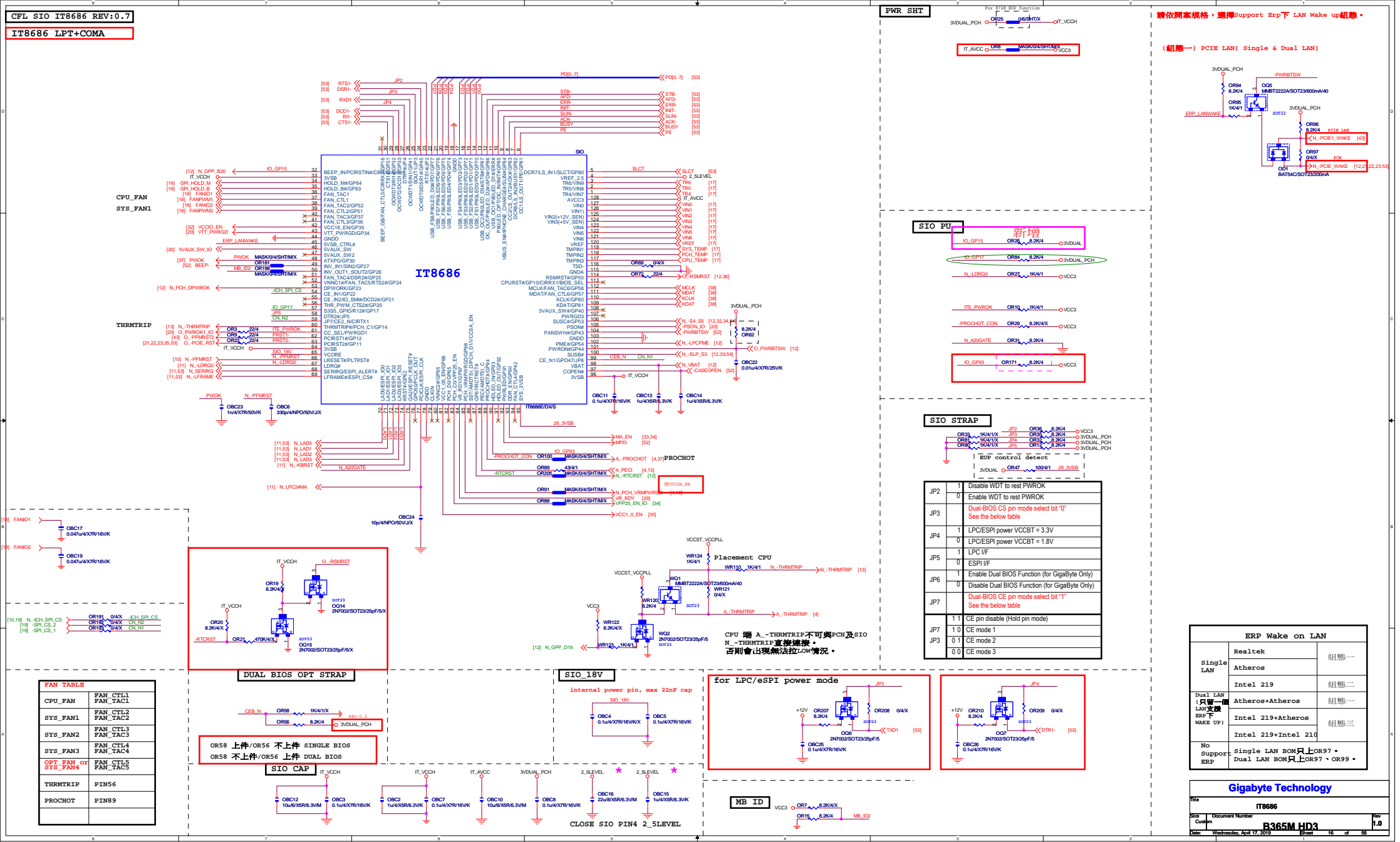


PCHI		
A25	VSS	A42
A30	VSS	D45
P22	VSS	BG44
AV38	VSS	BF44
AV45	VSS	BF45
AV8	VSS	BF2
AY11	VSS	W29
AY19	VSS	A35
AY37	VSS	A40
AY4	VSS	A41
AY42	VSS	AA17
AY8	VSS	AA18
B25	VSS	AA20
B3	VSS	AA21
B30	VSS	AA26
B35	VSS	AA28
B4	VSS	AA29
B41	VSS	AB17
BA13	VSS	AC32
BA17	VSS	AE4
BA29	VSS	AE8
BA31	VSS	AF18
BA37	VSS	AF20
BA4	VSS	AF21
BA42	VSS	AF25
BA40	VSS	AF28
BC38	VSS	AF29
BC40	VSS	AF4
BC9	VSS	AF42
BD11	VSS	D29
BD16	VSS	AG20
BD2	VSS	AG21
BD21	VSS	AG23
BD25	VSS	AG25
F2	VSS	AG26
F31	VSS	AG28
F6	VSS	AG29
F8	VSS	AH11
F39	VSS	AH13
F43	VSS	AH30
G4	VSS	AH32
G40	VSS	AH33
G42	VSS	AH38
F6	VSS	AJ1
G9	VSS	AJ17
H11	VSS	AJ18
H13	VSS	AJ20
H17	VSS	AJ21
H19	VSS	AJ23
H22	VSS	AJ25
H24	VSS	AJ26
H27	VSS	AJ28
H29	VSS	AJ29
H33	VSS	AJ45
H35	VSS	AK10
H38	VSS	AK14
H4	VSS	AK16
H42	VSS	AK17
H9	VSS	AK18
J4	VSS	AK28
M36	VSS	AK28
M38	VSS	AM14
M4	VSS	AN14
M8	VSS	AP19
M9	VSS	AR22
N13	VSS	AR27
N15	VSS	AU29
N19	VSS	AU33
N22	VSS	AV1
N24	VSS	AV10
N31	VSS	AV35
N42	VSS	AV24
P10	VSS	AV27
P12	VSS	AV33
AV35	VSS	

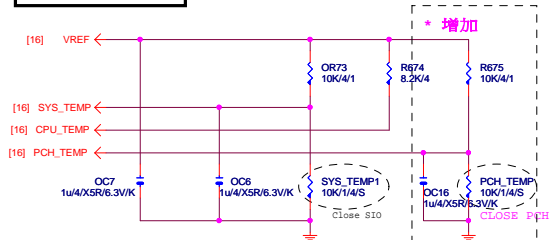
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GL82B365/S[10HB1-03B365-10R]

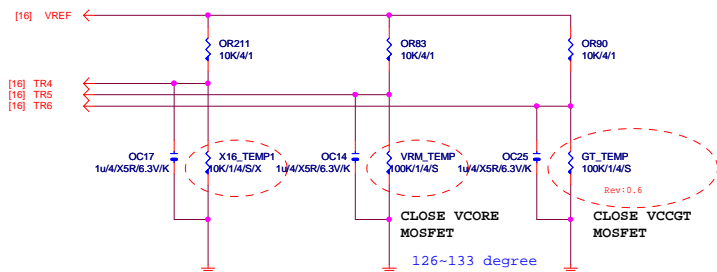
PCHL			
BD34	VSS[70]	VSS[1]	AB18
BD39	VSS[71]	VSS[2]	AB20
BD7	VSS[72]	VSS[3]	AB21
BE2	VSS[73]	VSS[4]	AB25
BF43	VSS[74]	VSS[5]	AB29
BF5	VSS[75]	VSS[6]	AB4
BG18	VSS[76]	VSS[7]	AB42
BG23	VSS[77]	VSS[8]	AC10
BG28	VSS[78]	VSS[9]	AC11
BG32	VSS[79]	VSS[10]	AC14
BG37	VSS[80]	VSS[11]	AC16
BG40	VSS[81]	VSS[12]	AC38
BG9	VSS[83]	VSS[13]	AC4
C1	VSS[84]	VSS[14]	AC5
A12	VSS[85]	VSS[15]	AC7
C2	VSS[86]	VSS[16]	AC8
A6	VSS[87]	VSS[17]	AD1
C9	VSS[88]	VSS[18]	AD18
D1	VSS[89]	VSS[19]	AD20
D10	VSS[90]	VSS[20]	AD21
D12	VSS[91]	VSS[21]	AD25
D15	VSS[92]	VSS[22]	AD29
D16	VSS[93]	VSS[23]	AD45
B12	VSS[94]	VSS[24]	AE11
D19	VSS[95]	VSS[25]	AE14
D21	VSS[96]	VSS[26]	AE32
D24	VSS[97]	VSS[27]	AE33
D25	VSS[98]	VSS[28]	AE38
D29	VSS[99]	VSS[29]	AK29
D30	VSS[100]	VSS[30]	AK30
D33	VSS[101]	VSS[31]	AK32
D35	VSS[102]	VSS[32]	AK35
D36	VSS[103]	VSS[33]	AK39
D39	VSS[104]	VSS[34]	AL4
D44	VSS[105]	VSS[35]	AL42
D7	VSS[106]	VSS[36]	AM10
P13	VSS[107]	VSS[37]	AM11
P15	VSS[108]	VSS[38]	AM13
P17	VSS[109]	VSS[39]	AM17
P19	VSS[110]	VSS[40]	AM19
P31	VSS[111]	VSS[41]	AM24
P33	VSS[112]	VSS[42]	AM27
P35	VSS[113]	VSS[43]	AM29
P4	VSS[114]	VSS[44]	AM32
P42	VSS[115]	VSS[45]	AM33
P8	VSS[116]	VSS[46]	AM4
R1	VSS[117]	VSS[47]	AN45
R32	VSS[118]	VSS[48]	AP10
T10	VSS[119]	VSS[49]	AP11
T14	VSS[120]	VSS[50]	AP13
T22	VSS[121]	VSS[51]	AP15
T29	VSS[122]	VSS[52]	AP22
T32	VSS[123]	VSS[53]	AP27
T36	VSS[124]	VSS[54]	AP31
T38	VSS[125]	VSS[55]	AP33
Y38	VSS[126]	VSS[56]	AP34
Y4	VSS[127]	VSS[57]	AP39
Y8	VSS[128]	VSS[127]	T4
T42	VSS[129]	VSS[143]	W26
T5	VSS[130]	VSS[128]	V16
U4	VSS[131]	VSS[129]	V17
U42	VSS[132]	VSS[130]	V18
V10	VSS[133]	VSS[131]	V30
V14	VSS[134]	VSS[132]	V32
W3	VSS[135]	VSS[133]	V33
AR13	VSS[136]	VSS[134]	V38
AR31	VSS[137]	VSS[135]	V4
AR33	VSS[138]	VSS[136]	V8
AR4	VSS[139]		W18
AT10	VSS[140]	VSS[138]	W20
AT13	VSS[141]	VSS[139]	W21
AT35	VSS[142]	VSS[140]	W23
AT37	VSS[143]	VSS[141]	W25
AT42	VSS[144]	VSS[142]	
AU11	VSS[145]		A44
AU17	VSS[146]	VSS1	BE1
BD30	VSS[147]	BD1	B1
W45	VSS[148]	VSS11	A2
Y13	VSS[149]	VSS14	B2
Y14	VSS[150]	VSS15	A3
Y30	VSS[151]	VSS16	B4
Y32	VSS[152]	VSS17	A4
Y33	VSS[153]	VSS18	B44
Y34	VSS[154]	VSS19	B45
Y35	VSS[155]	VSS2	
Y36	VSS[156]		
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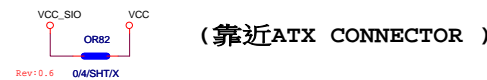
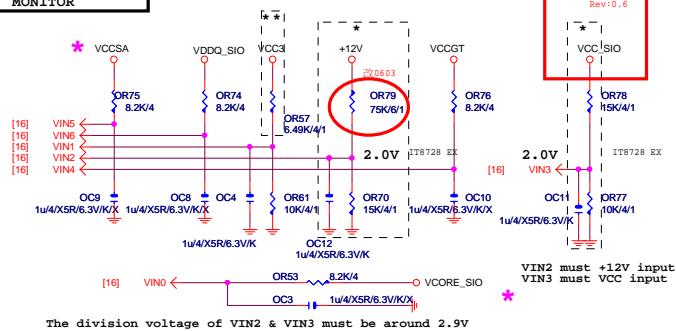
TEMP H/W MONITOR



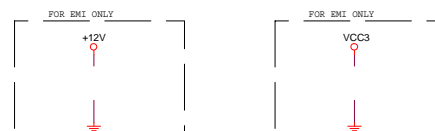
低階機種: 3個FAN時使用



VOLTAGE-- H/W MONITOR



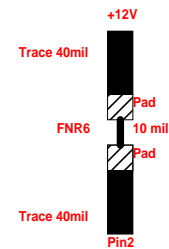
(靠近ATX CONNECTOR)



Gigabyte Technology

Title		HWM,KB/MS, FAN CTRL	
Size	Document Number	B365M HD3	
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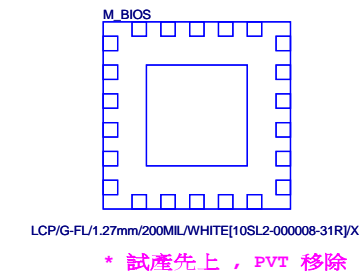
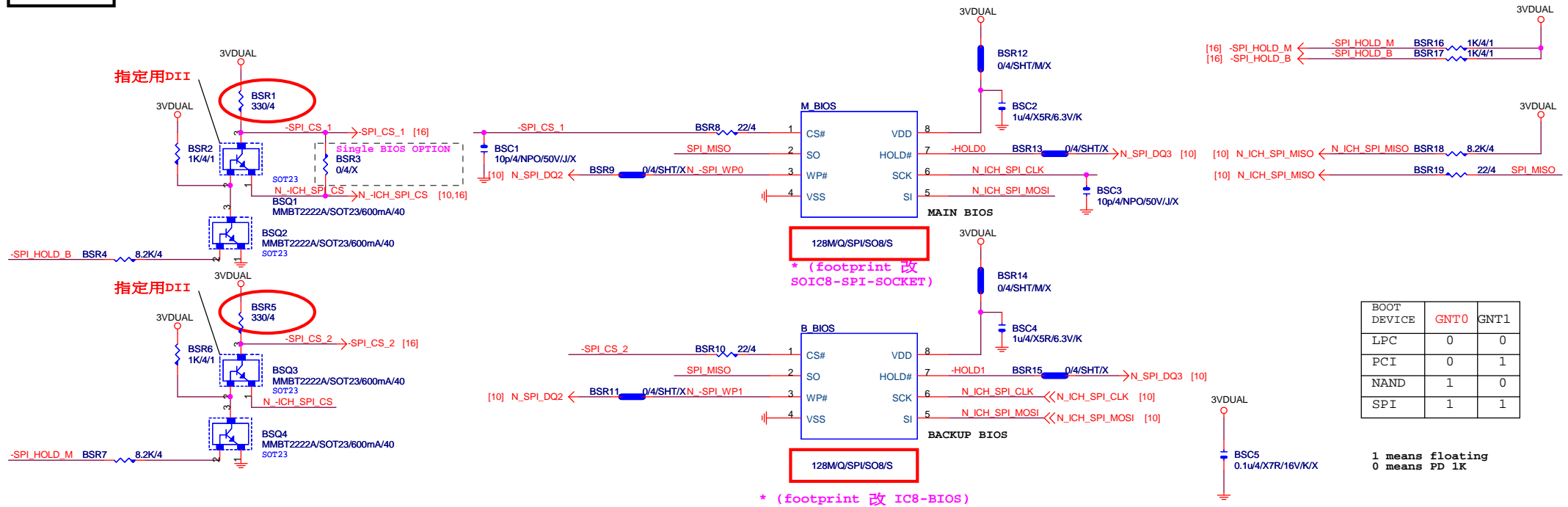
Rev: 0.8

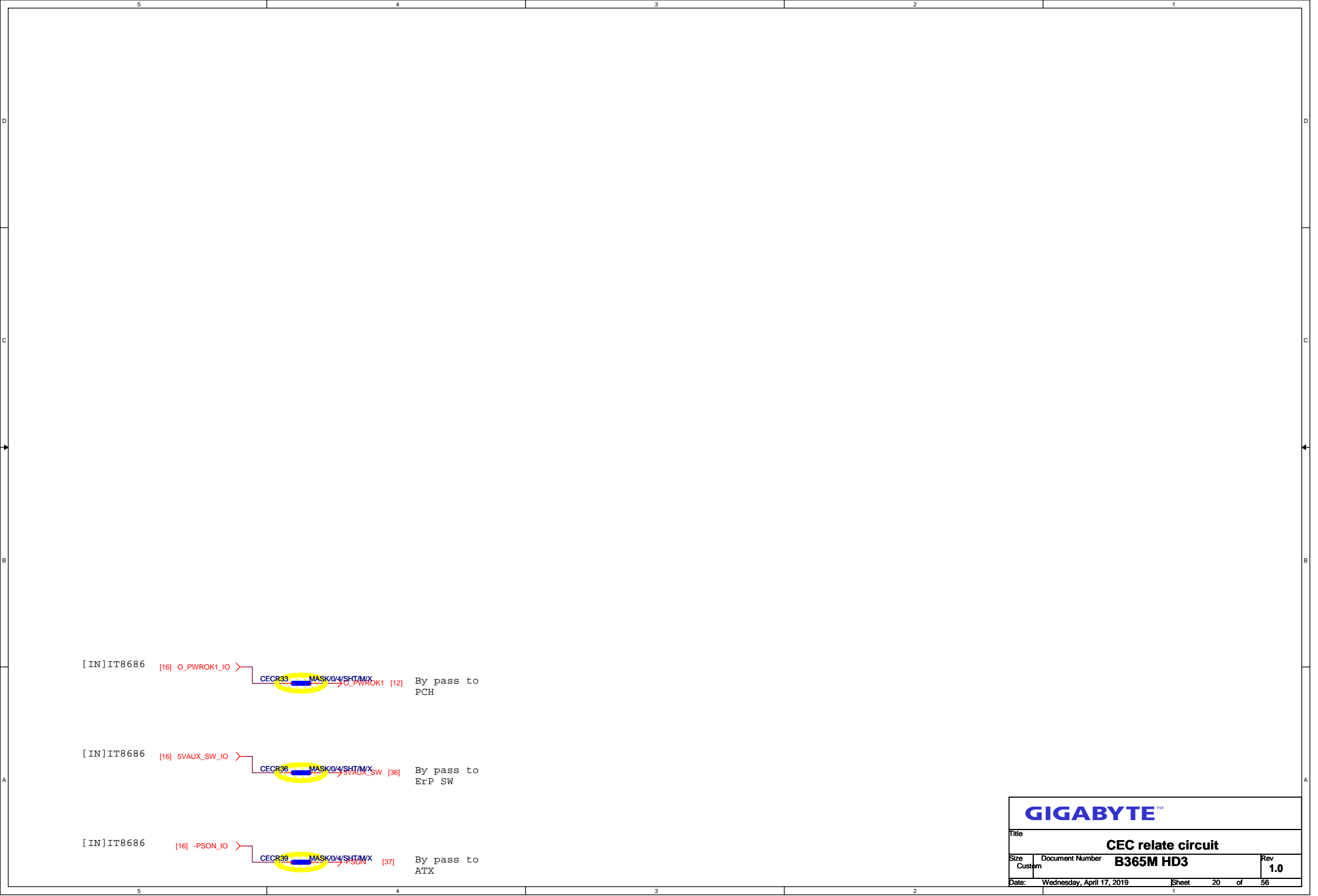


MODE: Floating=> Auto mode,
High=>PWM Mode,
Low=>Voltage Mode.

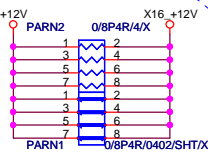
DUAL BIOS

MOSI For DMI RX Termination Voltage





+12 - protect
short-wire test



PA_EXP_RXP[0..15] >> PA_EXP_RXP[0..15] [4]
PA_EXP_RXN[0..15] >> PA_EXP_RXN[0..15] [4]
PA_EXP_TXP[0..15] >> PA_EXP_TXP[0..15] [4]
PA_EXP_TXN[0..15] >> PA_EXP_TXN[0..15] [4]

PA_EXP_TXP0	PAC5	0.22u/4/X5R/6.3V/K	PA_EXP_TXP0_C
PA_EXP_TXN0	PAC4	0.22u/4/X5R/6.3V/K	PA_EXP_TXN0_C
PA_EXP_TXP1	PAC6	0.22u/4/X5R/6.3V/K	PA_EXP_TXP1_C
PA_EXP_TXN1	PAC7	0.22u/4/X5R/6.3V/K	PA_EXP_TXN1_C
PA_EXP_TXP2	PAC8	0.22u/4/X5R/6.3V/K	PA_EXP_TXP2_C
PA_EXP_TXN2	PAC9	0.22u/4/X5R/6.3V/K	PA_EXP_TXN2_C
PA_EXP_TXP3	PAC10	0.22u/4/X5R/6.3V/K	PA_EXP_TXP3_C
PA_EXP_TXN3	PAC11	0.22u/4/X5R/6.3V/K	PA_EXP_TXN3_C
PA_EXP_TXP4	PAC12	0.22u/4/X5R/6.3V/K	PA_EXP_TXP4_C
PA_EXP_TXN4	PAC13	0.22u/4/X5R/6.3V/K	PA_EXP_TXN4_C
PA_EXP_TXP5	PAC14	0.22u/4/X5R/6.3V/K	PA_EXP_TXP5_C
PA_EXP_TXN5	PAC15	0.22u/4/X5R/6.3V/K	PA_EXP_TXN5_C
PA_EXP_TXP6	PAC16	0.22u/4/X5R/6.3V/K	PA_EXP_TXP6_C
PA_EXP_TXN6	PAC17	0.22u/4/X5R/6.3V/K	PA_EXP_TXN6_C
PA_EXP_TXP7	PAC18	0.22u/4/X5R/6.3V/K	PA_EXP_TXP7_C
PA_EXP_TXN7	PAC19	0.22u/4/X5R/6.3V/K	PA_EXP_TXN7_C
PA_EXP_TXP8	PAC20	0.22u/4/X5R/6.3V/K	PA_EXP_TXP8_C
PA_EXP_TXN8	PAC21	0.22u/4/X5R/6.3V/K	PA_EXP_TXN8_C
PA_EXP_TXP9	PAC22	0.22u/4/X5R/6.3V/K	PA_EXP_TXP9_C
PA_EXP_TXN9	PAC23	0.22u/4/X5R/6.3V/K	PA_EXP_TXN9_C
PA_EXP_TXP10	PAC24	0.22u/4/X5R/6.3V/K	PA_EXP_TXP10_C
PA_EXP_TXN10	PAC25	0.22u/4/X5R/6.3V/K	PA_EXP_TXN10_C
PA_EXP_TXP11	PAC26	0.22u/4/X5R/6.3V/K	PA_EXP_TXP11_C
PA_EXP_TXN11	PAC27	0.22u/4/X5R/6.3V/K	PA_EXP_TXN11_C
PA_EXP_TXP12	PAC28	0.22u/4/X5R/6.3V/K	PA_EXP_TXP12_C
PA_EXP_TXN12	PAC29	0.22u/4/X5R/6.3V/K	PA_EXP_TXN12_C
PA_EXP_TXP13	PAC30	0.22u/4/X5R/6.3V/K	PA_EXP_TXP13_C
PA_EXP_TXN13	PAC31	0.22u/4/X5R/6.3V/K	PA_EXP_TXN13_C
PA_EXP_TXP14	PAC32	0.22u/4/X5R/6.3V/K	PA_EXP_TXP14_C
PA_EXP_TXN14	PAC33	0.22u/4/X5R/6.3V/K	PA_EXP_TXN14_C
PA_EXP_TXP15	PAC34	0.22u/4/X5R/6.3V/K	PA_EXP_TXP15_C
PA_EXP_TXN15	PAC35	0.22u/4/X5R/6.3V/K	PA_EXP_TXN15_C

PCIEX16:16/5/5/5/16

PCI-E REV:1.1--> 2.5GHZ

PCE-E X1(單向) BANDWITH=2.5GHz*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWITH=2.5GHz*(8b/10b)X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWITH=2.5GHz*(8b/10b)X16=32Gb/s=4GB/s

PCE-E X16(雙向) BANDWITH=2.5GHz*(8b/10b)X16X2=64Gb/s=8GB/s

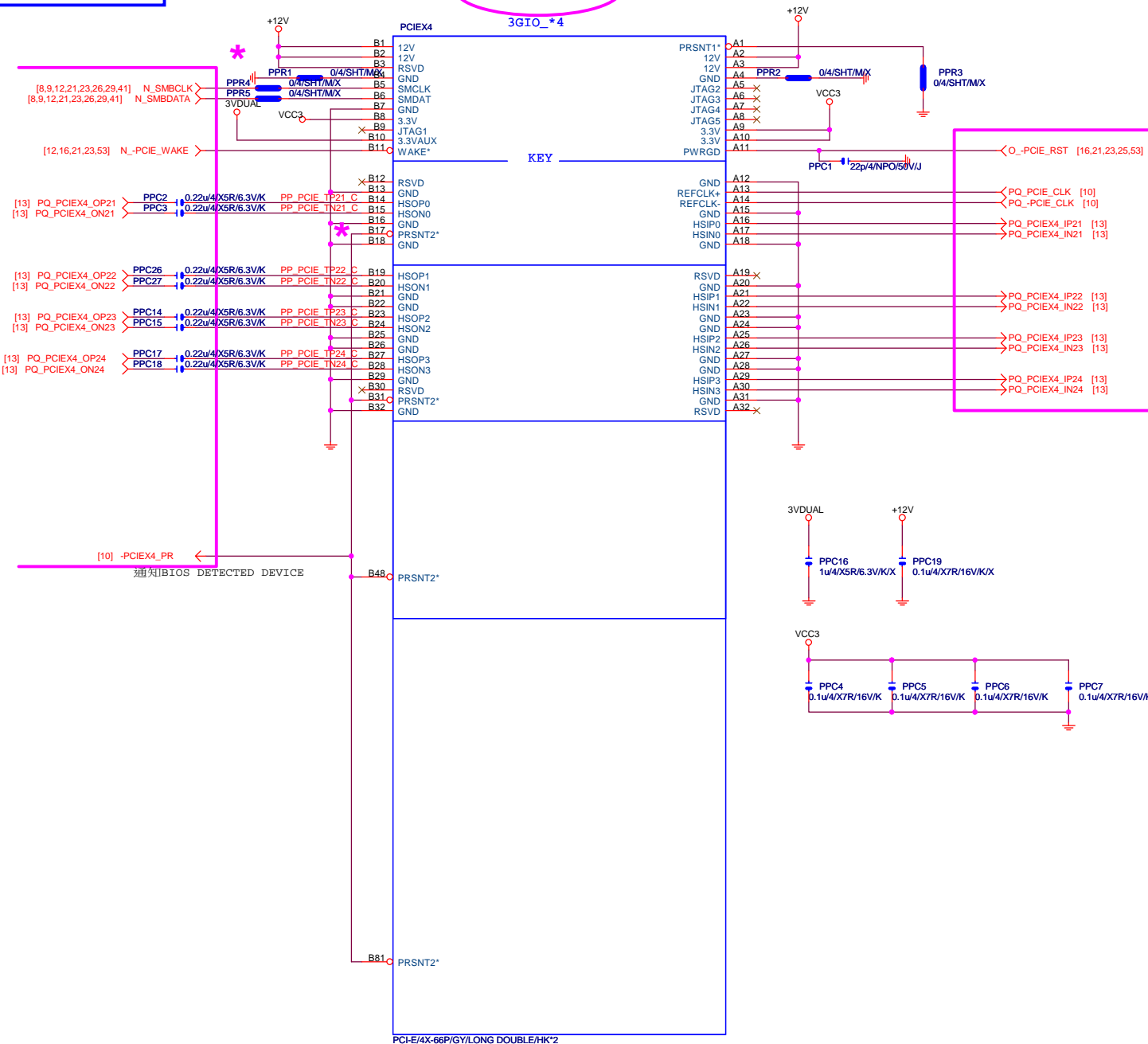
PCI-E REV:2.0--> 5GHZ

PCIESLOT-164P

3GIO_*16

PCI-E/16X-164P/GY/LONG DOUBLE/HK*2

灰色SLOT



灰色

Gigabyte Technology

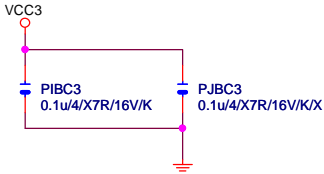
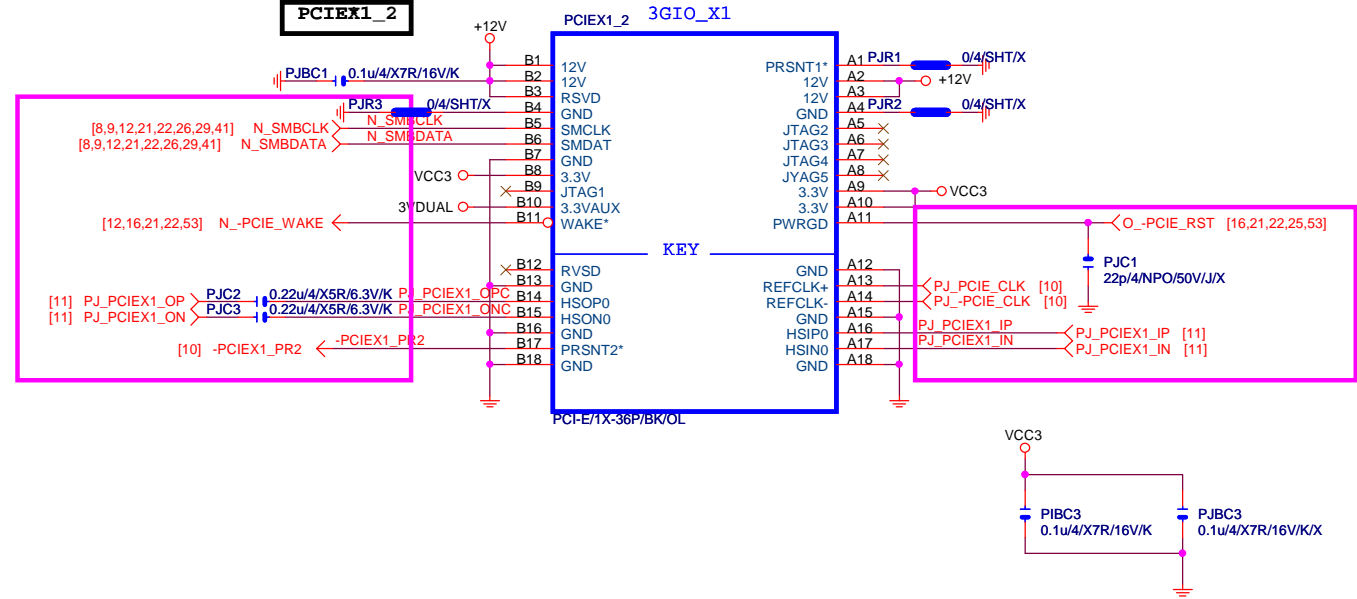
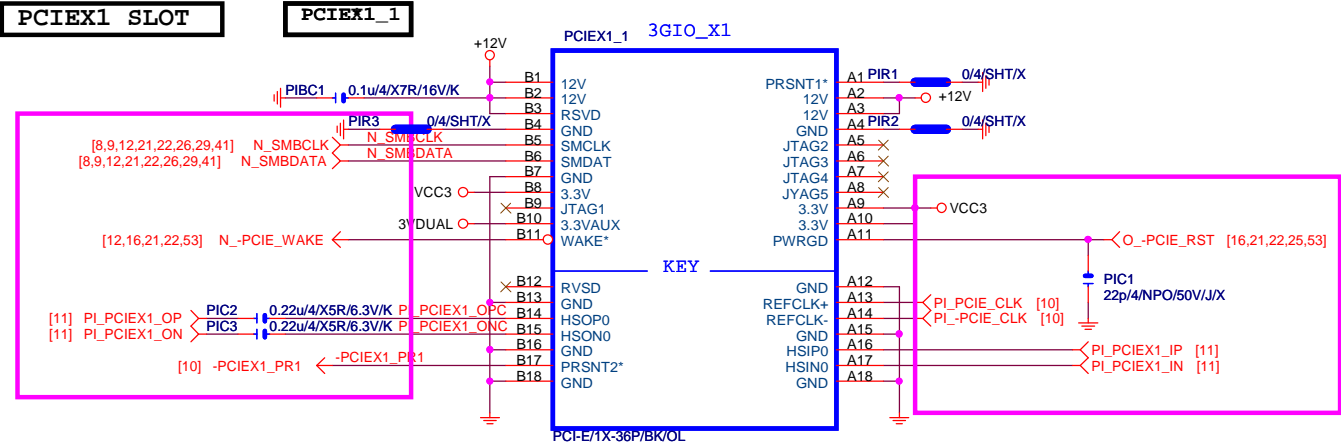
PCIE X4

B365M HD3

Rev 1.0

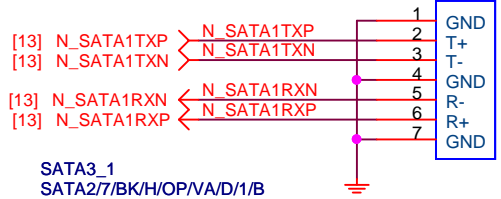
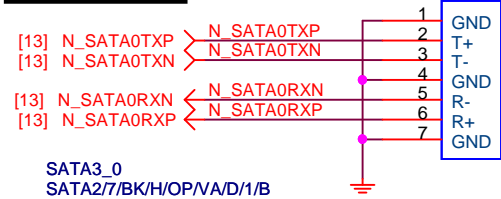
Date: Wednesday, April 17, 2019

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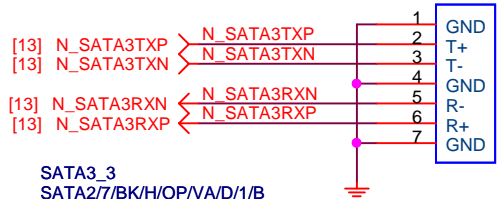
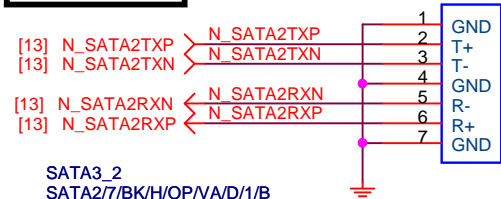
SATA3 0/1

BLACK CONNECTOR



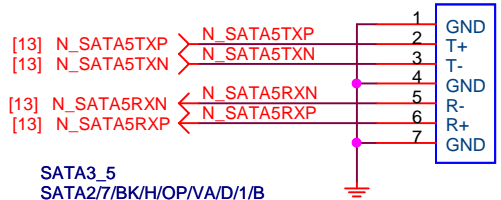
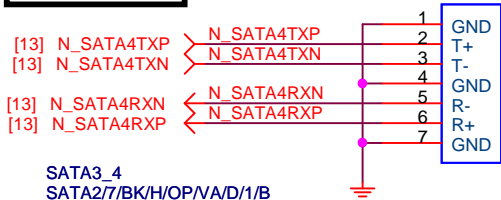
SATA3 2/3

BLACK CONNECTOR



SATA3 4/5

BLACK CONNECTOR



Gigabyte Technology

Title			SATA		
Size	Document Number				Rev
Custom	B365M HD3				1.0
Date:	Wednesday, April 17, 2019		Sheet	24	of 56

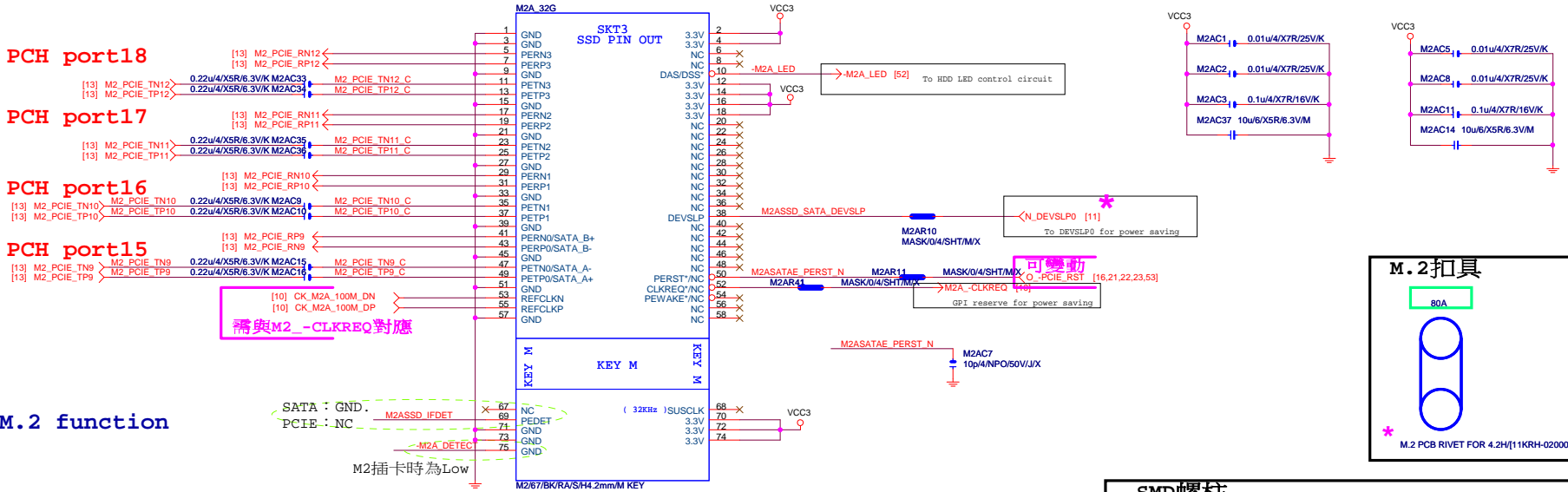
Rev 0.6

M.2 Lane4 from PCH port18

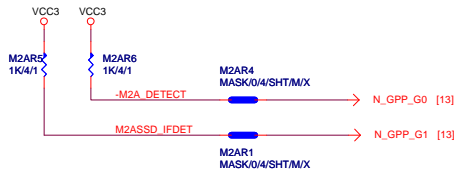
M.2 Lane3 from PCH port17

M.2 Lane2 from PCH port16

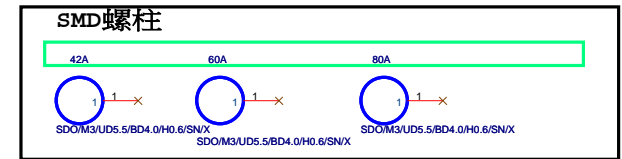
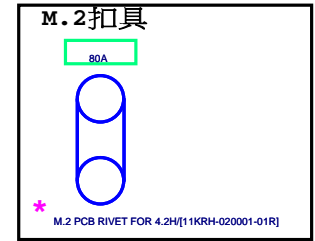
M.2 Lane1 from PCH port15



支援SATA and M.2 function

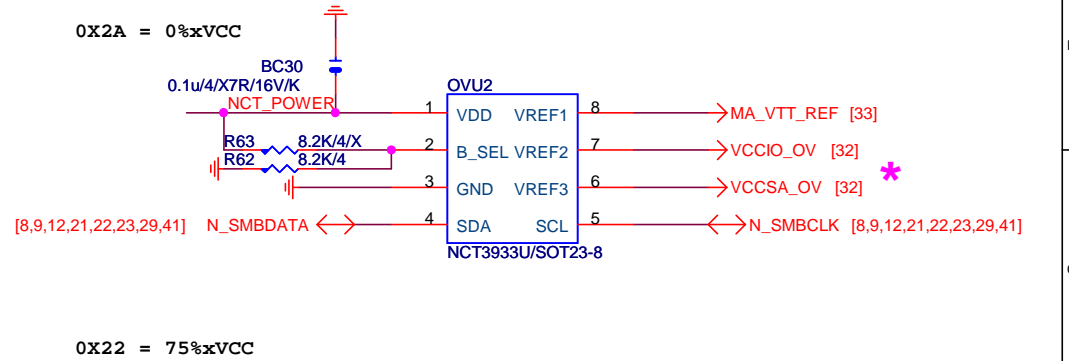
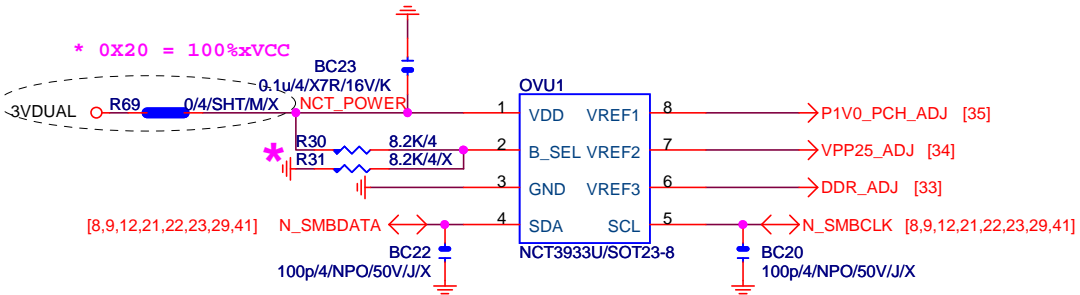


* Footprint : NGFF-M-75P-8CM-09MM-SMD



* Footprint : HOLE_165NP

OVER VOLTAGE



* 删除 OVU3

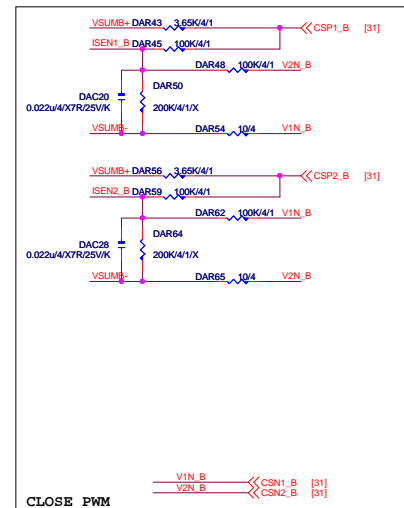
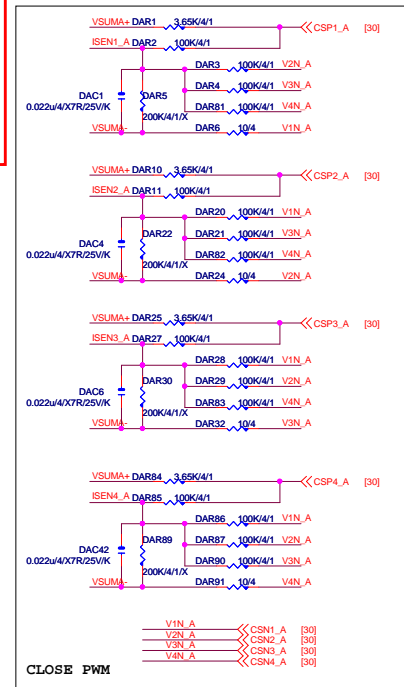
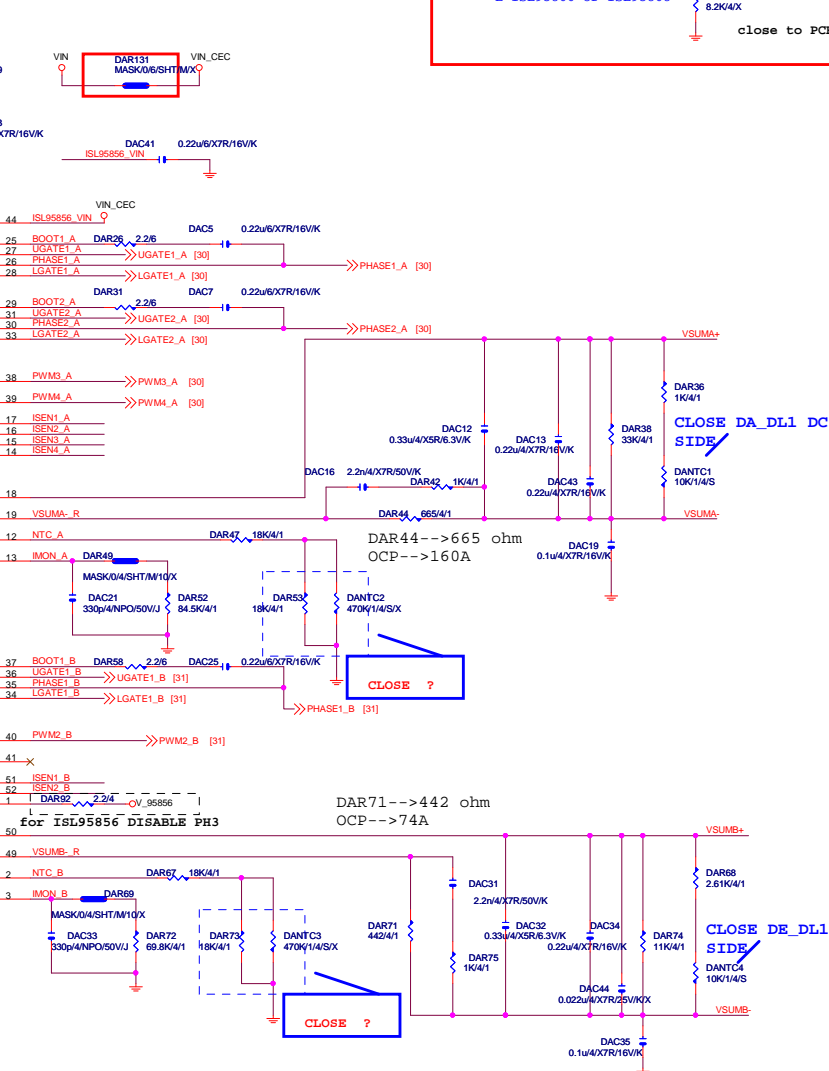
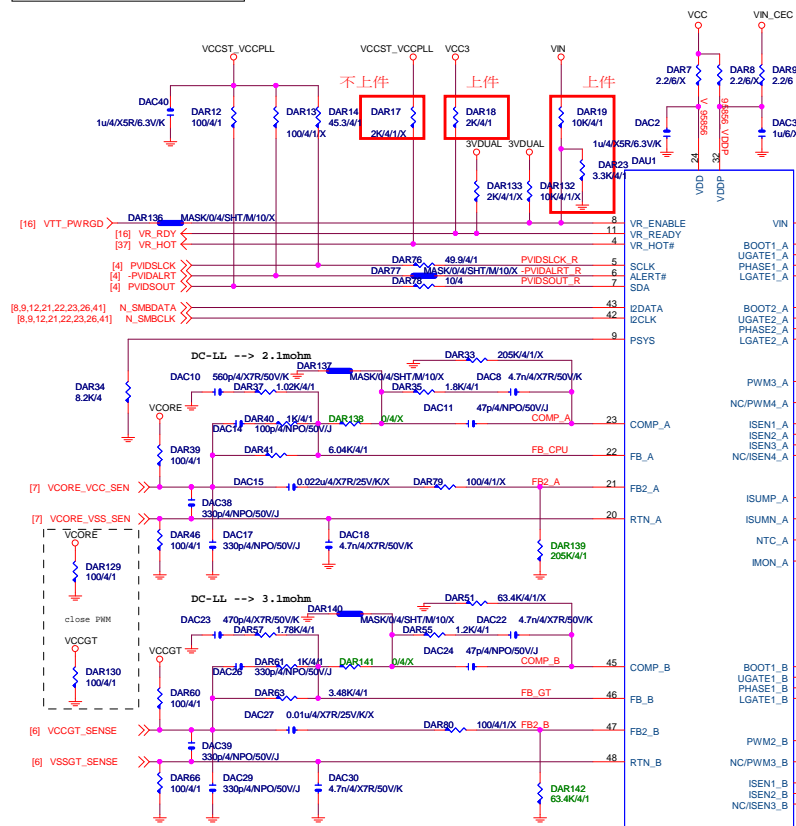
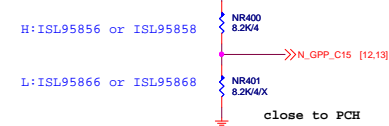
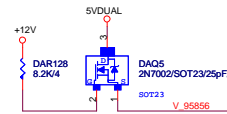
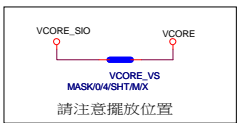
NCT3933	0X2A	0X20	0X22
VREF1	DDRVTT	VREF_DDRA_DQ	PCH Core
VREF2	VREF_DDRA_CA	N/A	VCC1_5_PCH
VREF3	VREF_DDRA_CA	VREF_DDRB_DQ	SMREF

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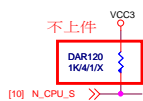
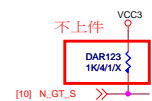
Title: CPU CORE VR-2

Size: Custom Document Number: B365M HD3 Rev: 1.0

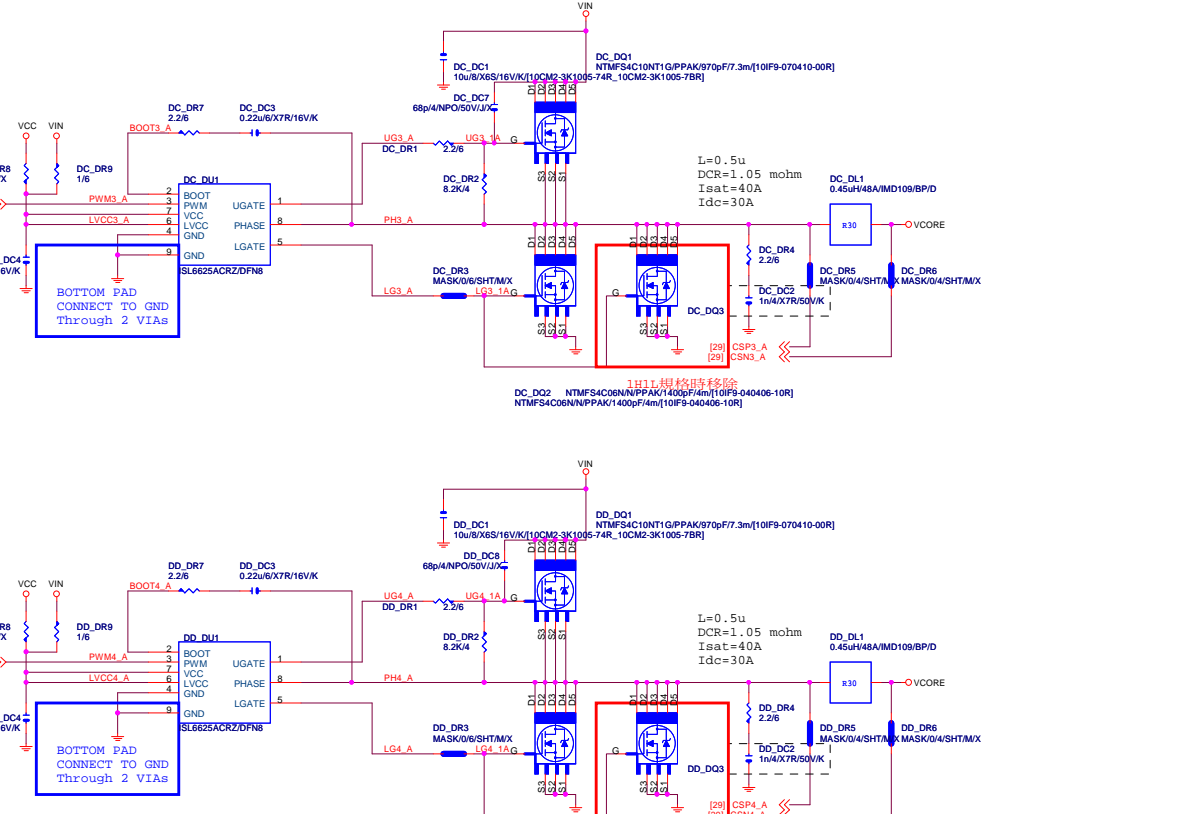
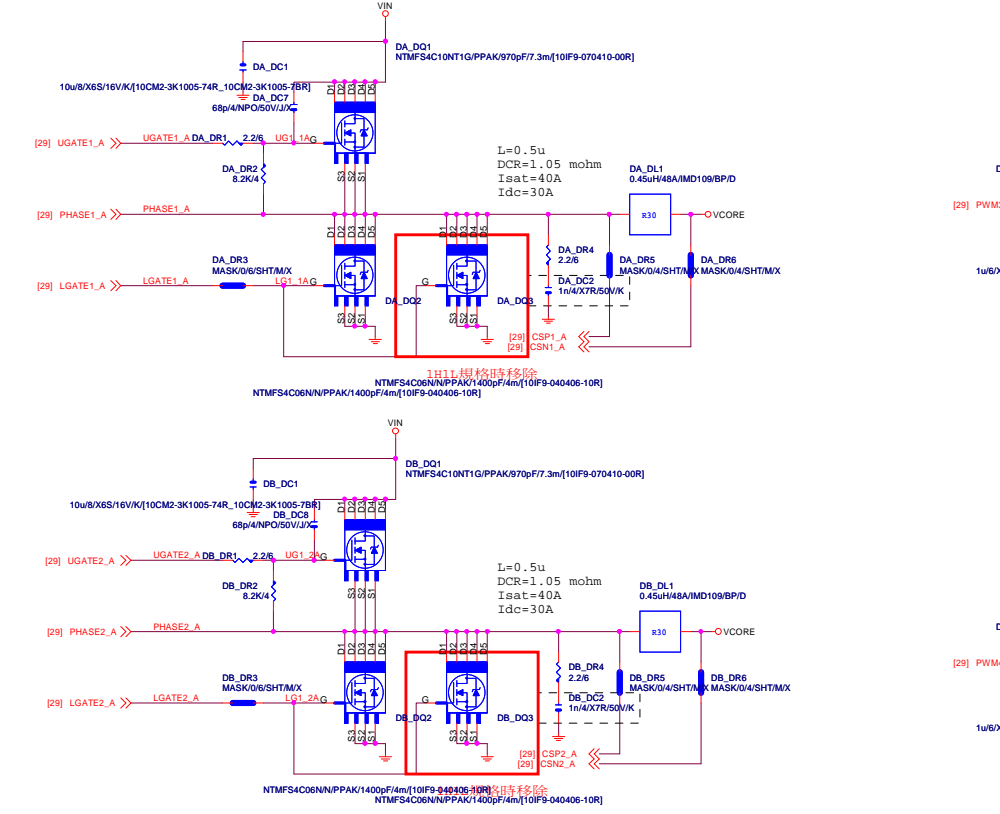
Date: Wednesday, April 17, 2019 Sheet 26 of 56



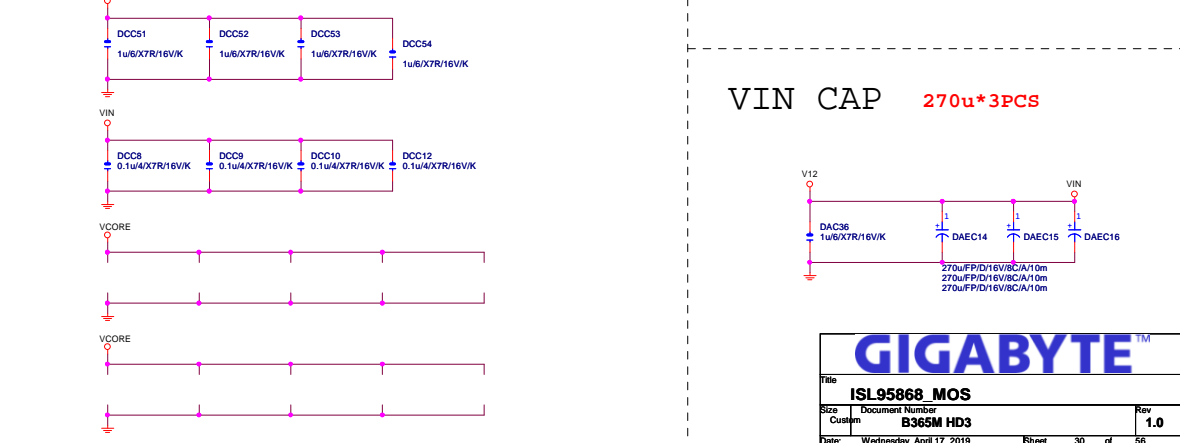
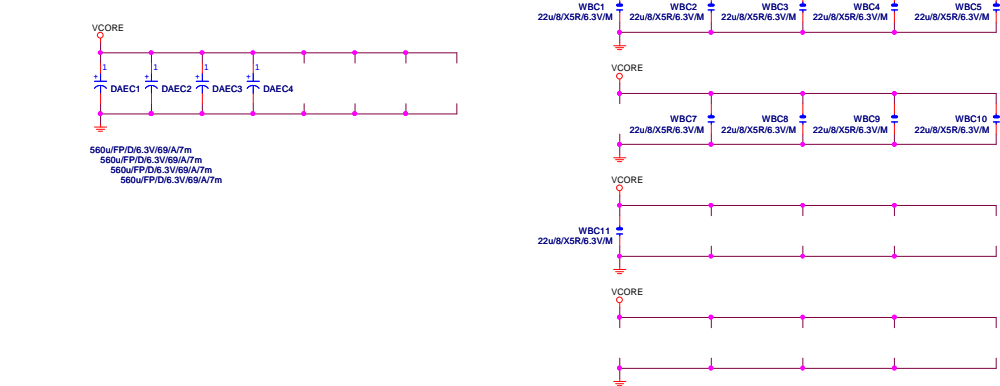
VSCORE	ISL95856	ISL95866	VCCGT	ISL95856	ISL95866
DAR137	X	V	DAR140	X	V
DAR138	V	X	DAR141	V	X
DAR139	X	V	DAR142	X	V
DAC15	V	X	DAC27	V	X
DAR79	V	X	DAR80	V	X
DAR33	V	X	DAR51	V	X



VCORE

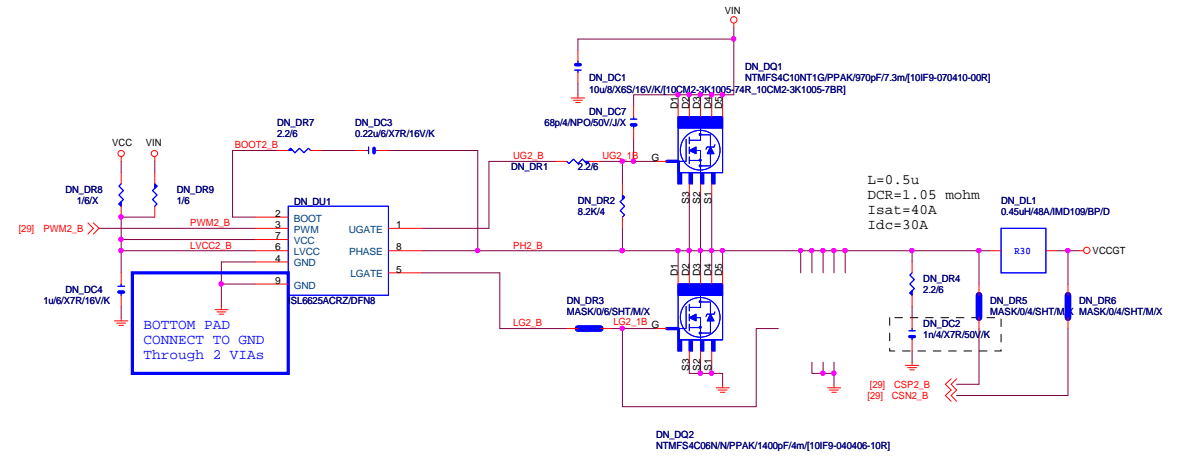
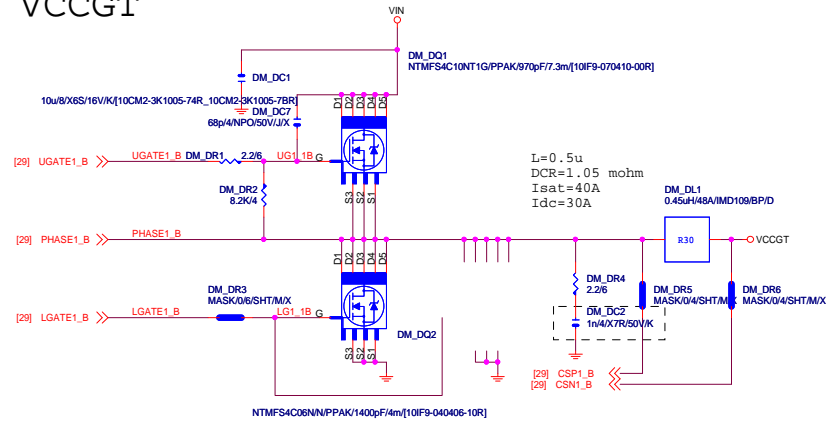


VCORE CAP 560u*6PCS
22u*29PCS

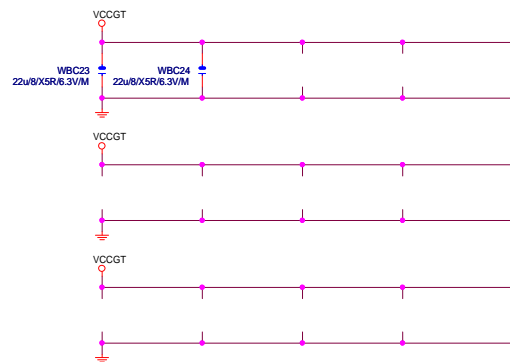
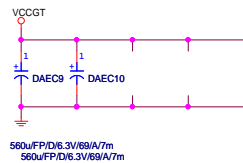


REV::0.4

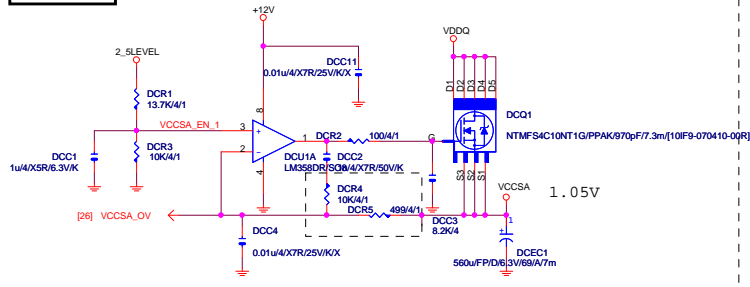
VCCGT



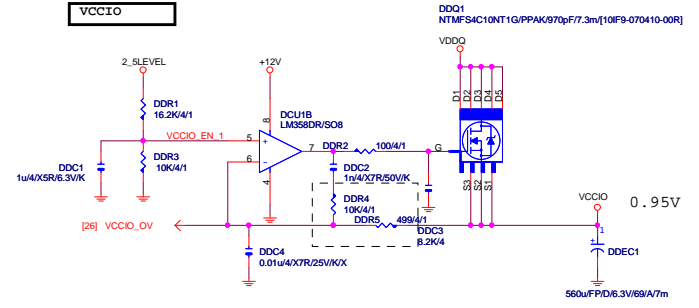
VCCGT	CAP	560u*3PCS 22u*15PCS
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VCCSA

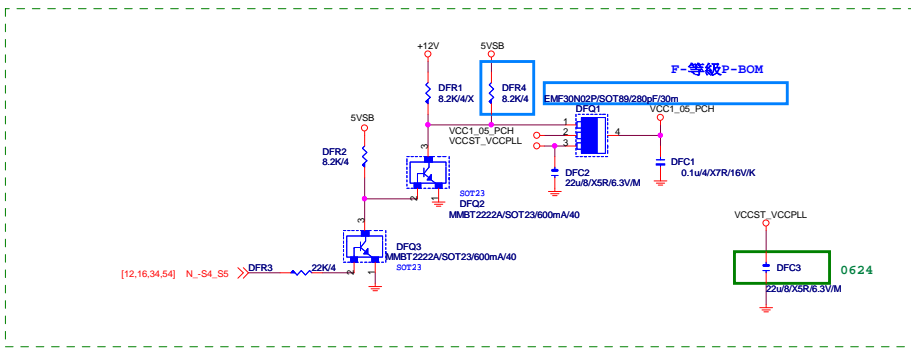


VCCIO

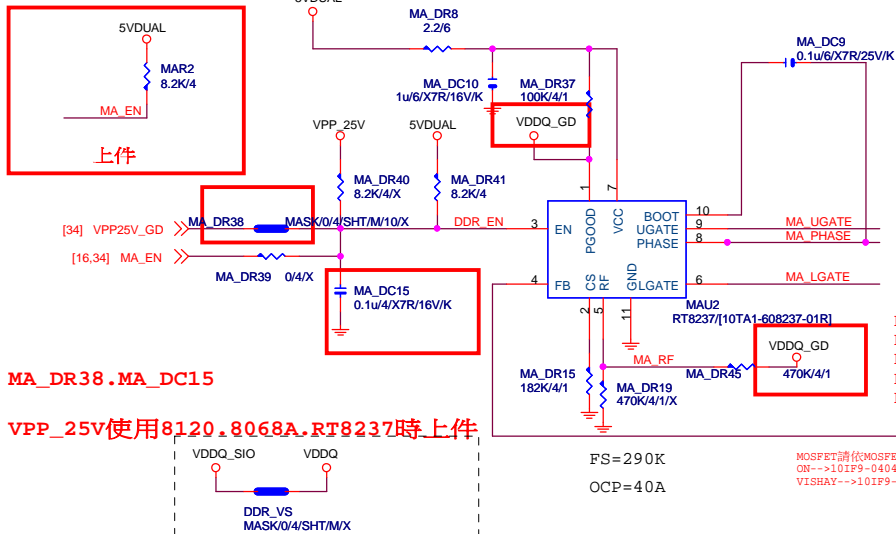


VCCIO_EN 1 DDR8  SHT/AMX VCCIO_EN [16]
Connect to IT8686

VCCST_VCCPLL

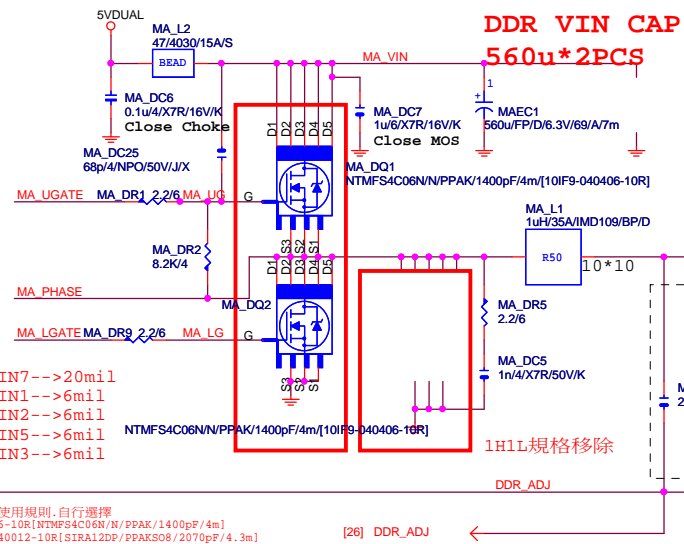


DDR4



MA_DR38.MA_DC15

VPP_25V使用8120.8068A.RT8237時上件



CHOKES與CAP料號可變

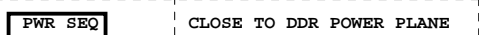
SUPPORT DDR4 1.2V

25A MAX

L=1u
DCR=2.5 mohm
Isat=35A
Idc=28A

請放置CHOKE一出來位置.先預留.
請自行確認ripple後再決定是否上件
use請從最重的負載端點拉回

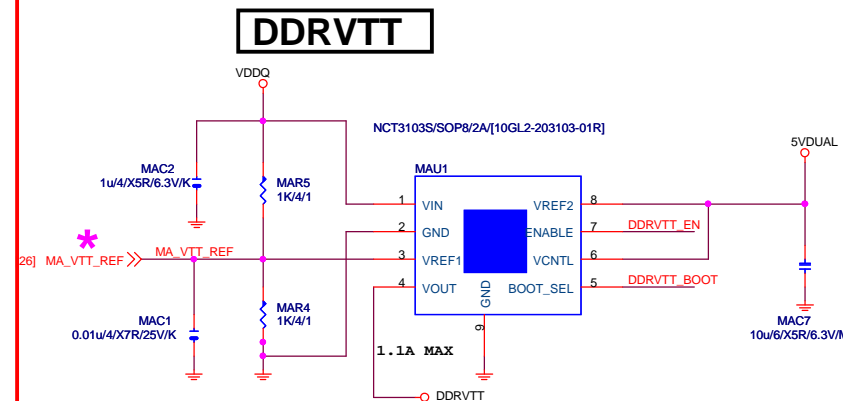
Remote sense 請從最重的負載端點拉回



For power sequence require

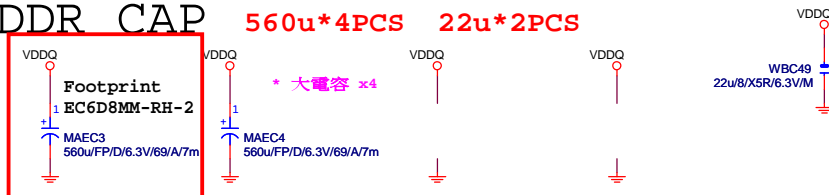
VPP_25V使用8120時上件

MAU1上RT9045時上件(不可MASK)



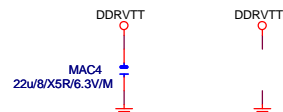
DDR CAP

560u*4PCS 22u*2PCS



DDRVTT CAP

* 大電容 x0



[4] DDR_VTT_CTL >>> DDR VTT CTL MAR110 MASK/0/4/SHT/M/10/X DDRVTT_EN
[12:16:54] N_SLP_S3 >>> N -SLP S3 MAR111 MASK/0/4/SHT/M/10/X DDRVTT_BOOT

GIGABYTE

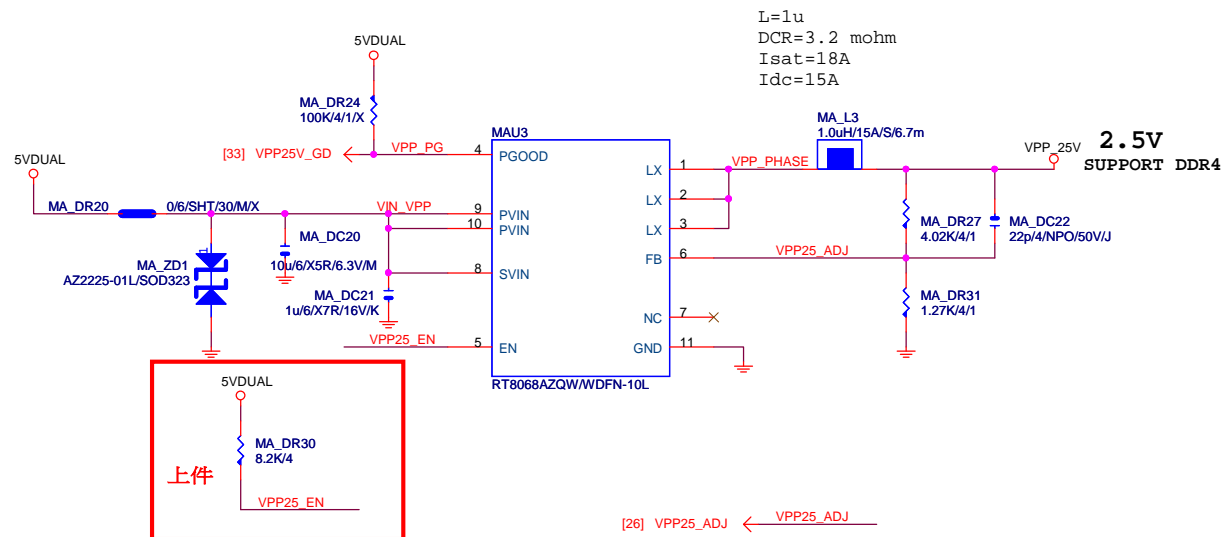
Title **RT8237 DDR4 POWER**

Size	Document Number	Rev
Custom	B365M HD3	1.0
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REV: 0.1

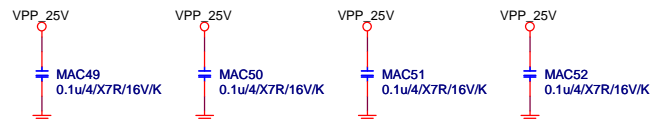
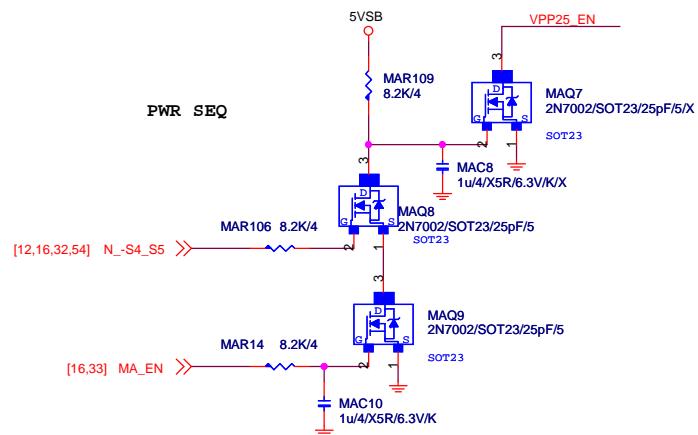
VPP 25V

CHOKES與CAP料號可變



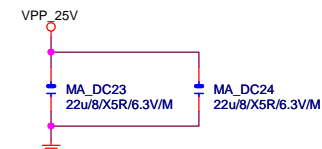
PWR SEQ

* 删除 MA_DR32



VPP CAP 22u*1PCS

* 大電容 x0



[16] VPP25_EN_IO >> MASK/0/4/SHT/M/X MAR114 VPP25_EN

GIGABYTE™

Title **RT8068A VPP25 POWER**

Size	Document Number	Rev
Custom	B365M HD3	1.0

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REV:0.5

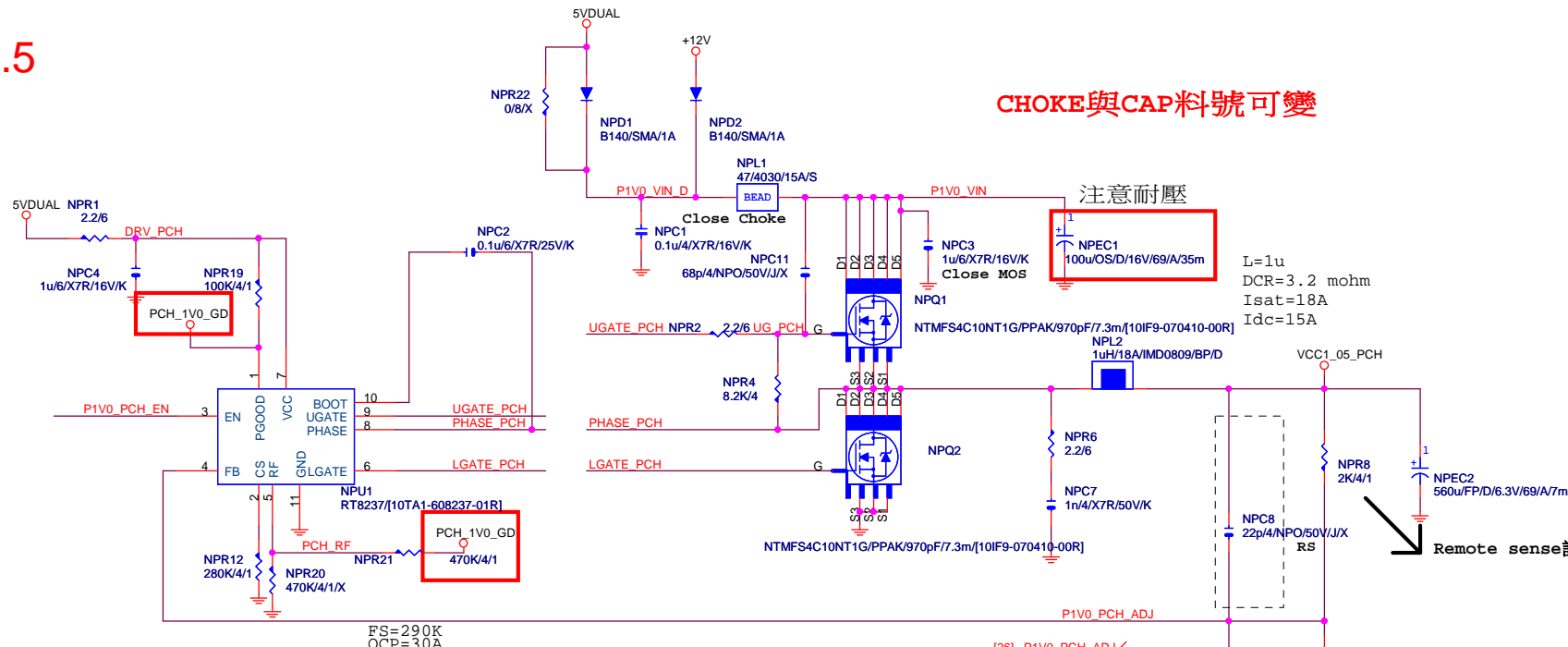
CHOKE與CAP料號可變

注意耐壓

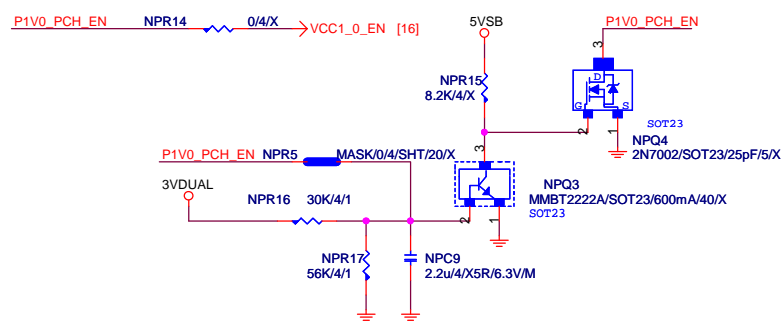
L=1u
DCR=3.2 mohm
Isat=18A
Idc=15A

Remote sense請從最重的負載端點拉回

請放置CHOKE一出來的地方

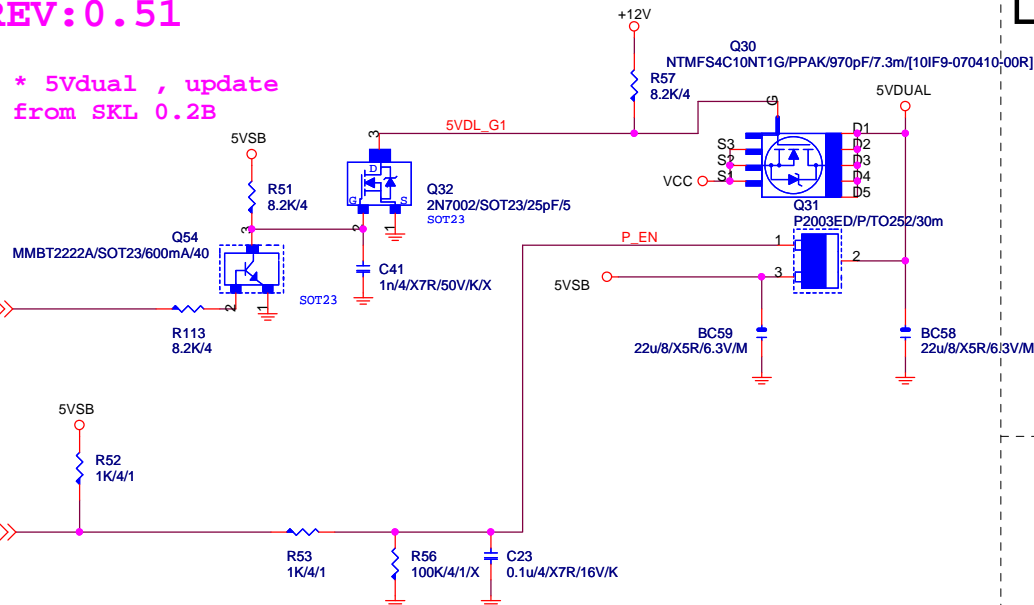


PWR SEQ



GIGABYTE™			
RT8237_PCH POWER			
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[20] 5VAUX SW



5VDUAL

3VDUAL

BC27
0.1u/4/X7R/16V/K

3VDUAL

R37
100/4/1

R38
169/4/1

Q4
L1085DG/TO252/5A

R36

C9
22u/8/X5R/6.3V/M

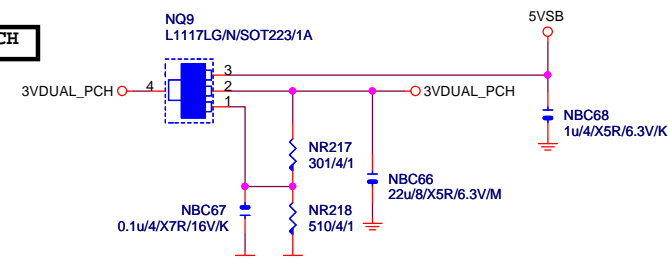
C8
1n/4/X7R/50V/K

O_RSMRST [12,16]

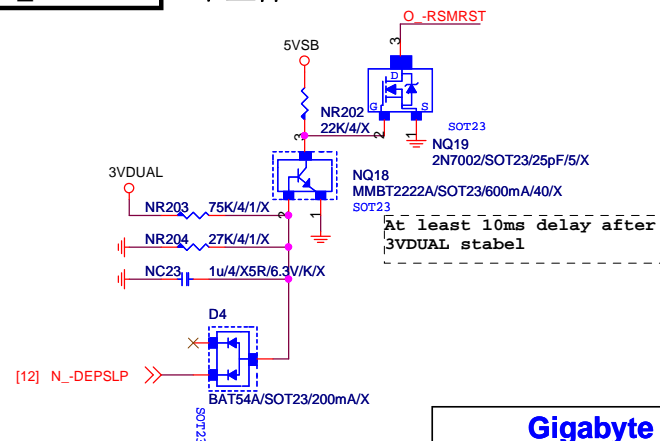
上22u 電容

Meet the rise time

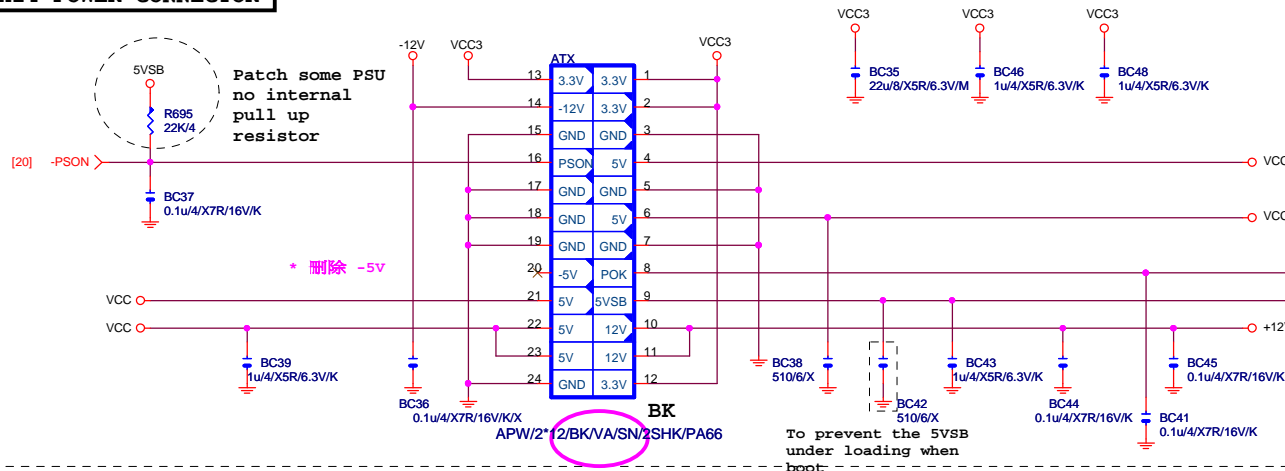
Rise:20% - 80%
Fall :2V- 0.8V



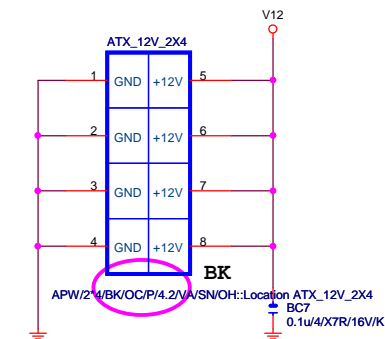
O_-RSMRST	(不上件)
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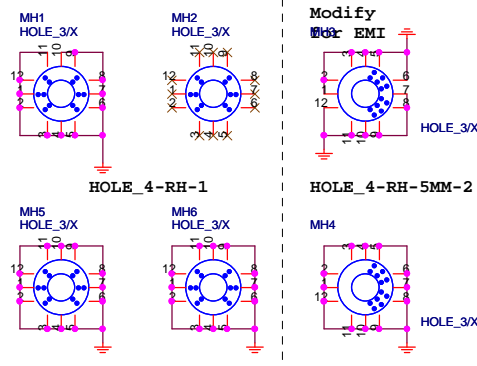
ATXX24 POWER CONNECTOR



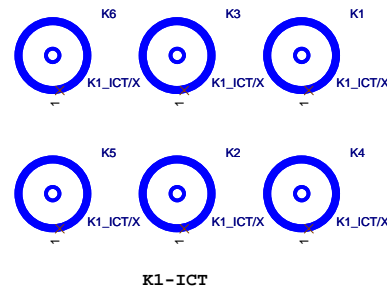
ATXX4 POWER CONNECTOR



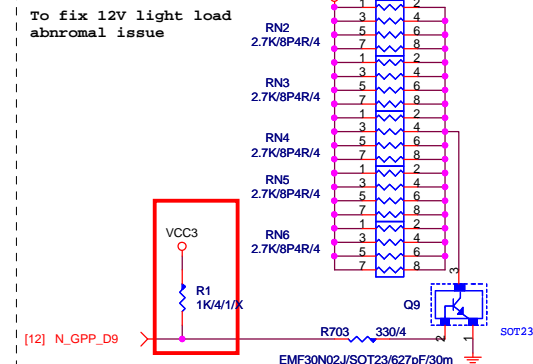
螺絲孔



固定孔/光學點

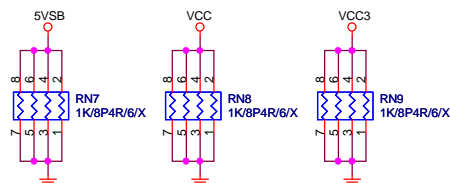


+12V DUMMY LOAD

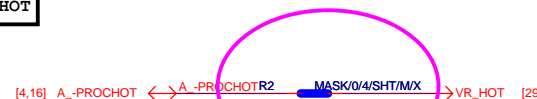


【技術通報R&D技術通報153】

DUMMY LOAD



-PROHOT



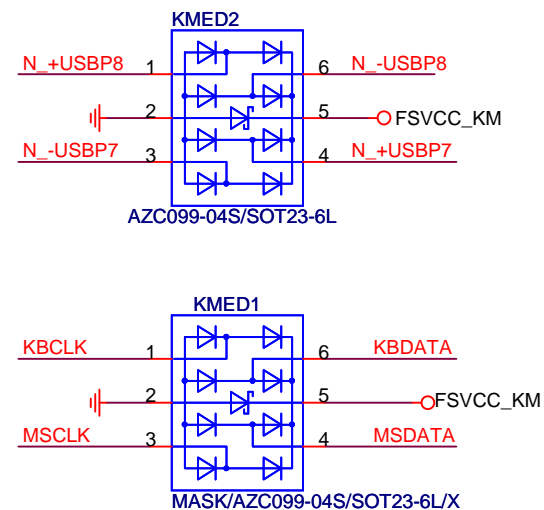
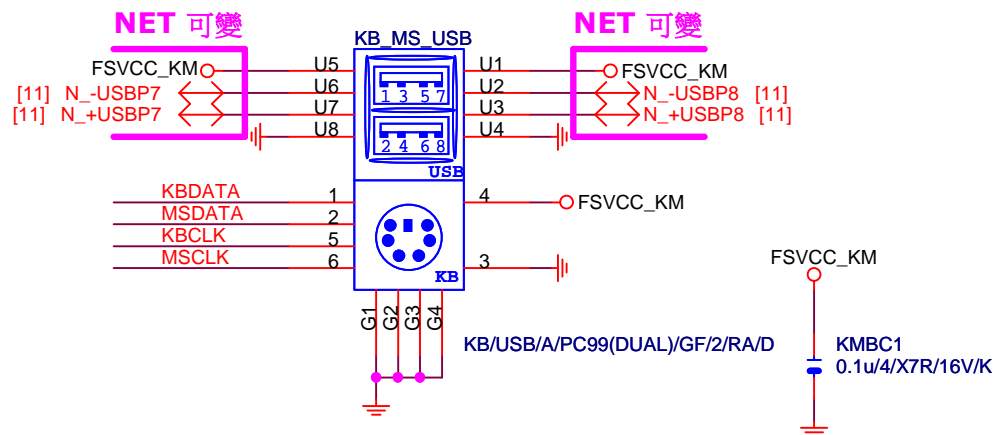
COUPON



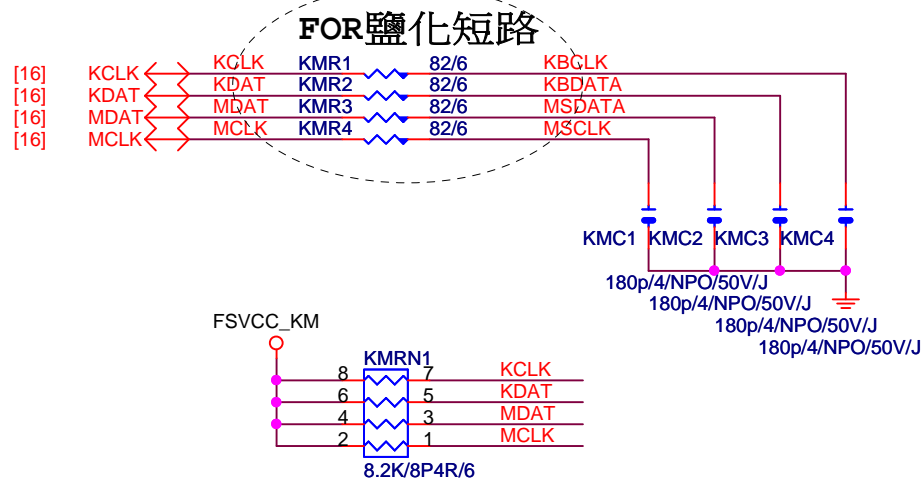
KB_MS_USB

Rev: 0.7

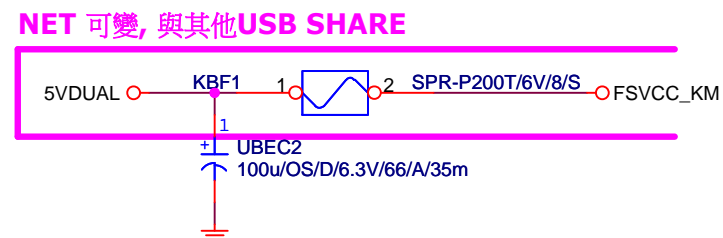
ESD



KB_MS_USB DAMPING/PU



KB_MS_USB PWR



USB OC PROTECT

Gigabyte Technology

Title

KB_MS_USB

Size
A

Document Number

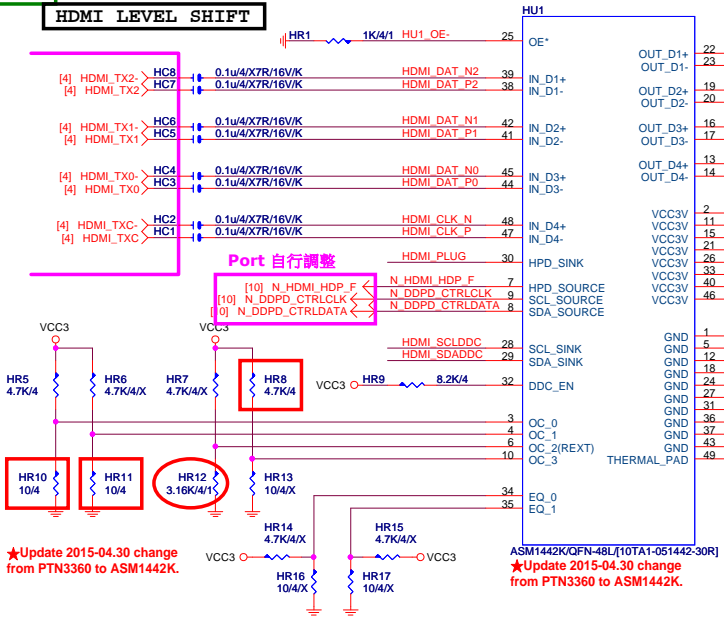
B365M HD3

Rev
1.0

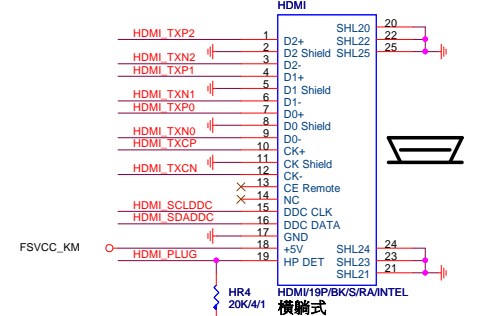
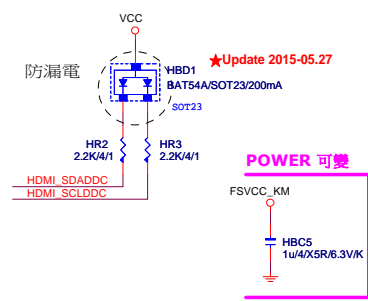
Date: Wednesday, April 17, 2019

Sheet 38 of 56

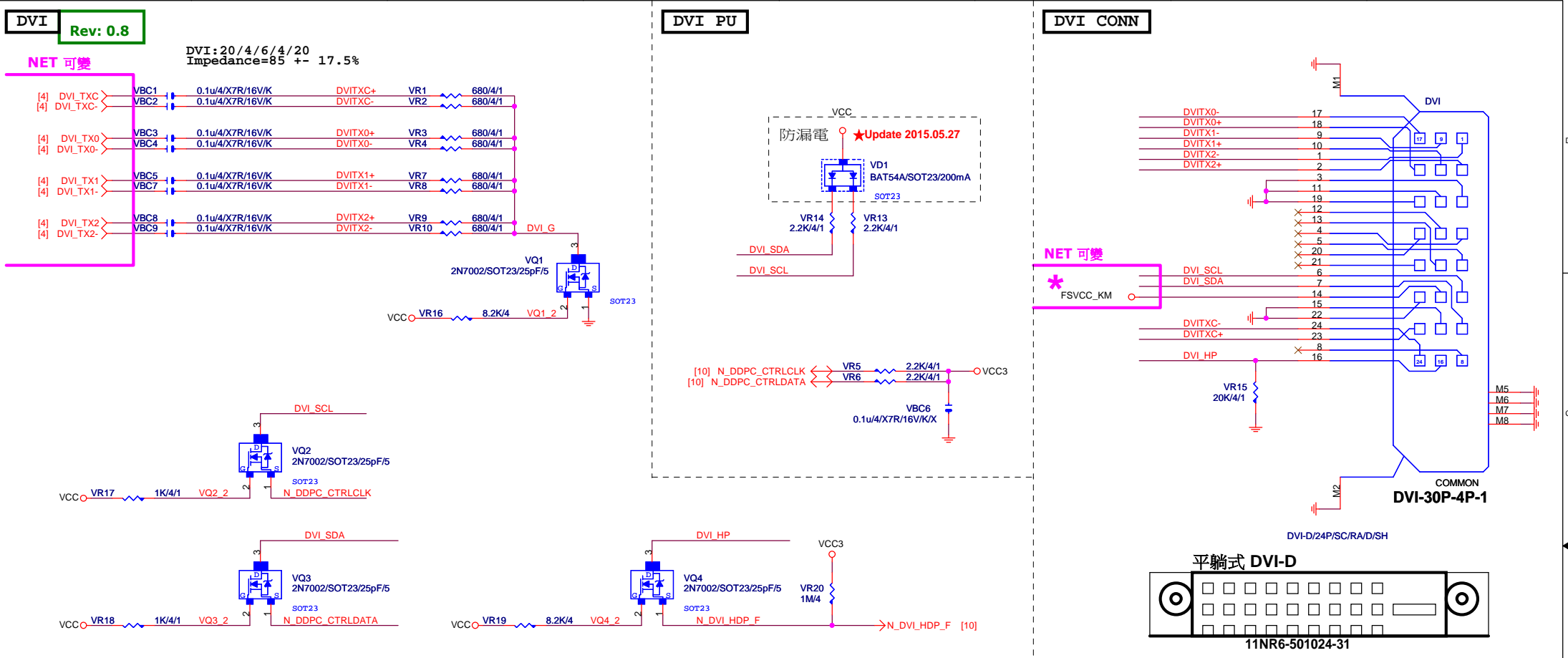
HDMI LEVEL SHIFT



【技術通報R&D技術通報150】
HDMI eye diagram 1.4版(deep color)會fail
原因: 因目前的HDMI訊號過長,造成RISING TIME過慢,而會壓到eye diagram
改善: ASMEDIA ASM1442 : 3.16K(PIN6 PULL DOWN電阻) 10ohm(PIN4 PULL DOWN電阻)

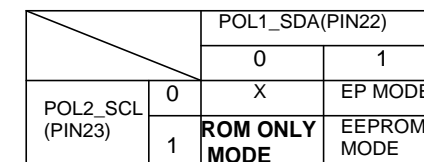


PTN3360:PIN 4/10/34/35 NC PIN,都不上值;只上HR12:10K
ASM1442:紅色框要上,HR12:3.16K

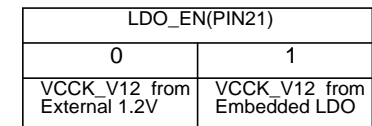




POWER



Embedded LDO

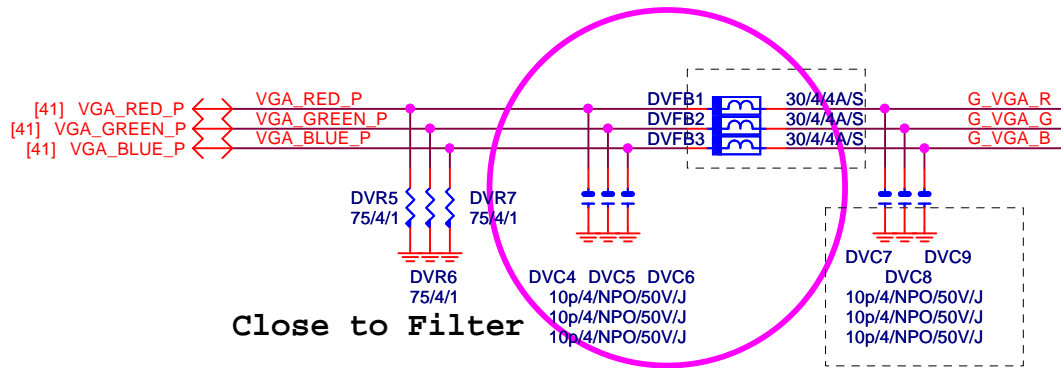
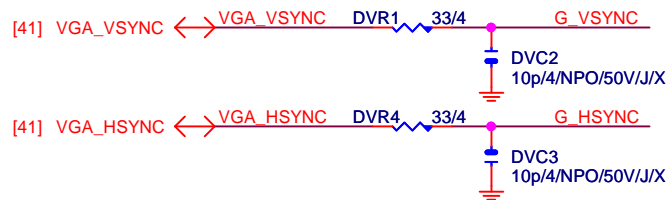
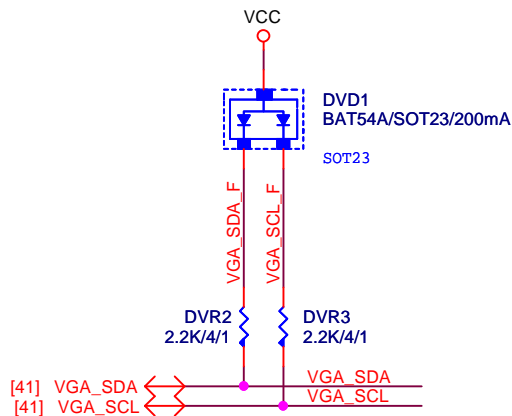


DP HPD

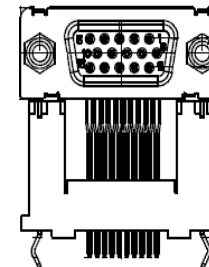
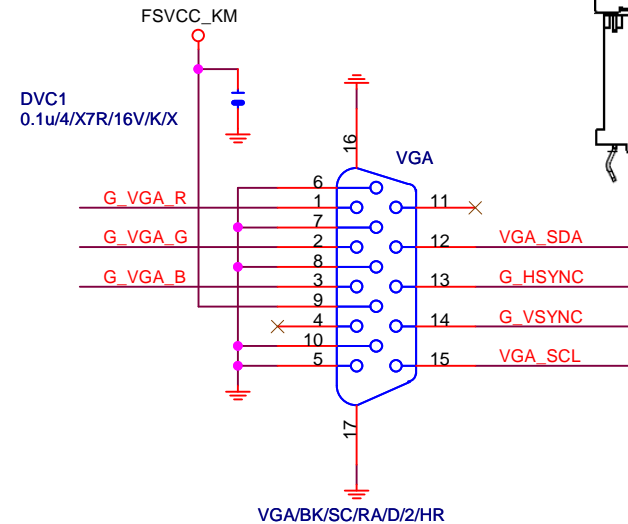


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Date: Wednesday, April 17, 2019	Sheet 41 of 56	

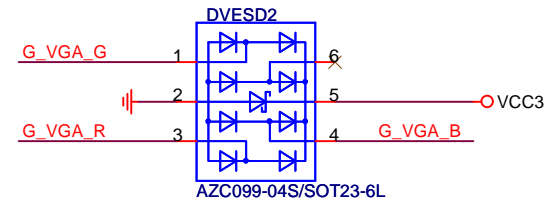
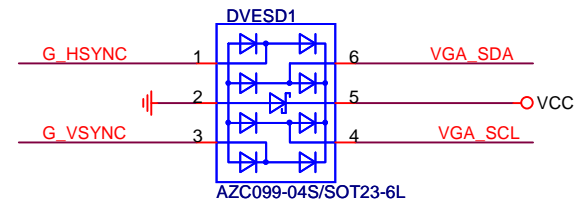
VGA SIGNAL R2.0



VGA CONN. 架高型VGA (BLACK)

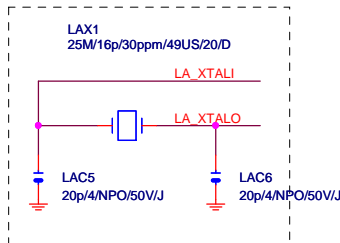


VGA ESD



Gigabyte Technology
DP-VGA RTD2168

Title		Document Number		Rev
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(CLOSE LAU1 PIN22, 30, 3, 8)

PIN22 LABC2 1u4/X5R/6.3V/K

PIN30 LABC9 0.1u4/X7R/16V/K

PIN3 LABC3 0.1u4/X7R/16V/K

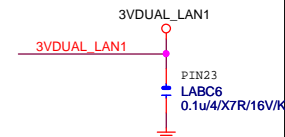
PIN8 LABC8 0.1u4/X7R/16V/K

LA_DVDD10

LA DVDD10

```
LABC2:1U CLOSE PIN22[REALTEK REQ]
```

note: lan power連接及電流



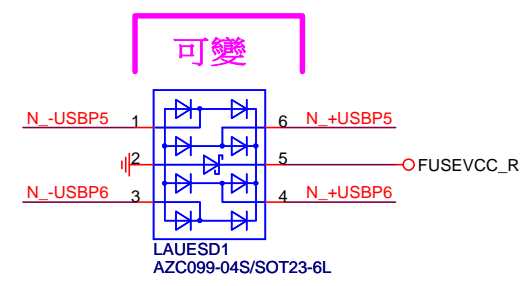
(CLOSE LAU1 PIN23)

LABC18,27:CLOSE PIN11[REALTEK SURGE]

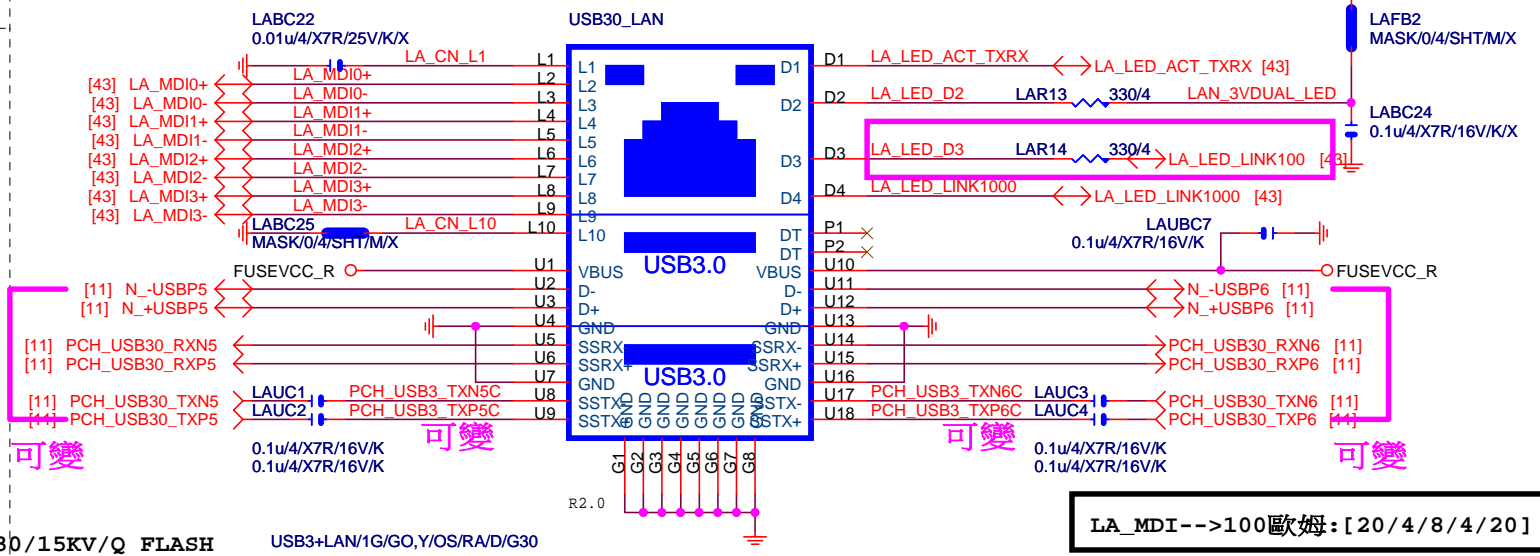
LABC14,20:CLOSE PIN32[REALTEK SURGE]

USB30_LAN CONNECTOR R2.01

RMA ESD PROTECT note:可變更USB NAME

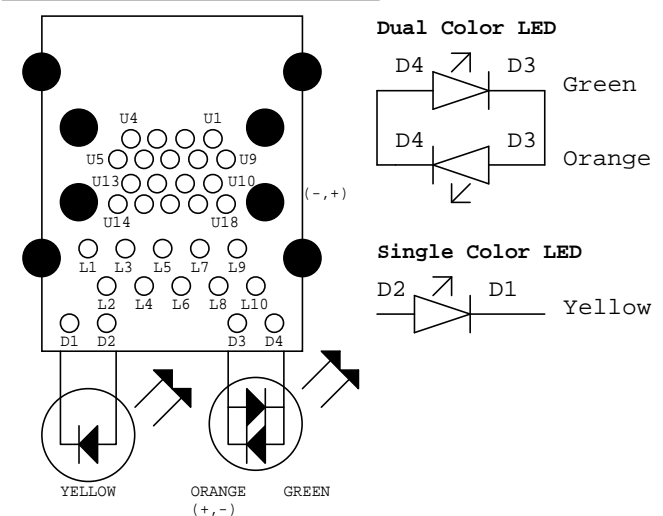


USB_LAN CONNECTOR [RTL8111G] note:可變更USB NAME

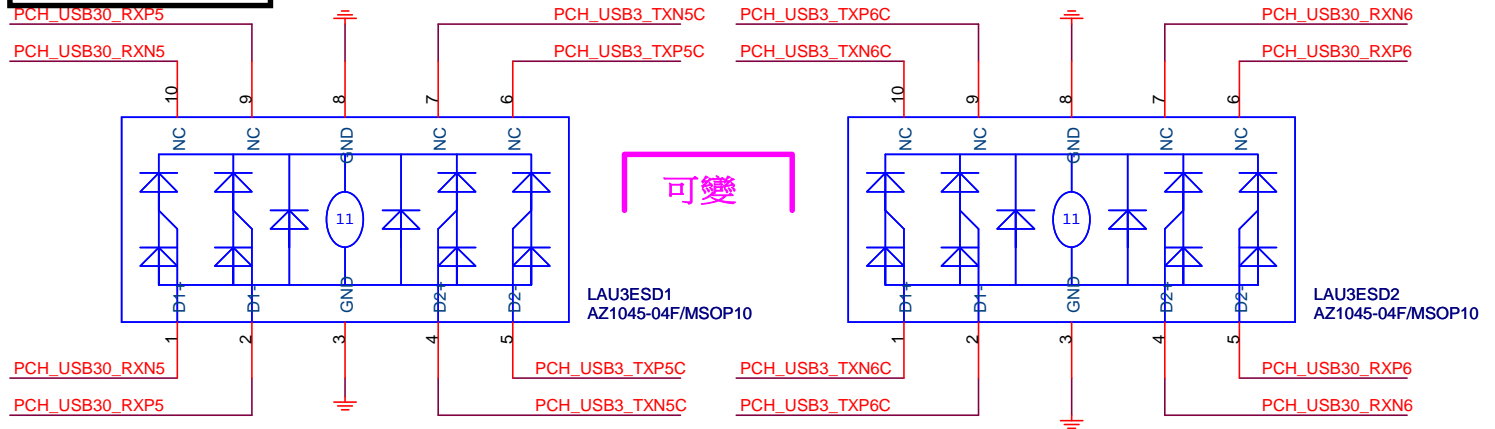


USB上白下藍:USB3+LAN/1G/GO,Y/OS/RA/D/G30/15KV/Q FLASH USB3+LAN/1G/GO,Y/OS/RA/D/G30

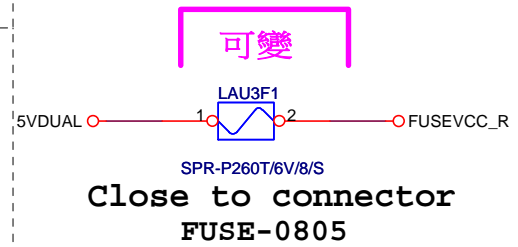
USB30_LAN LAYOUT示意圖



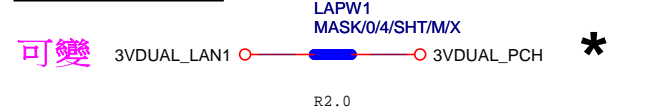
RMA ESD PROTECT note:可變更USB NAME



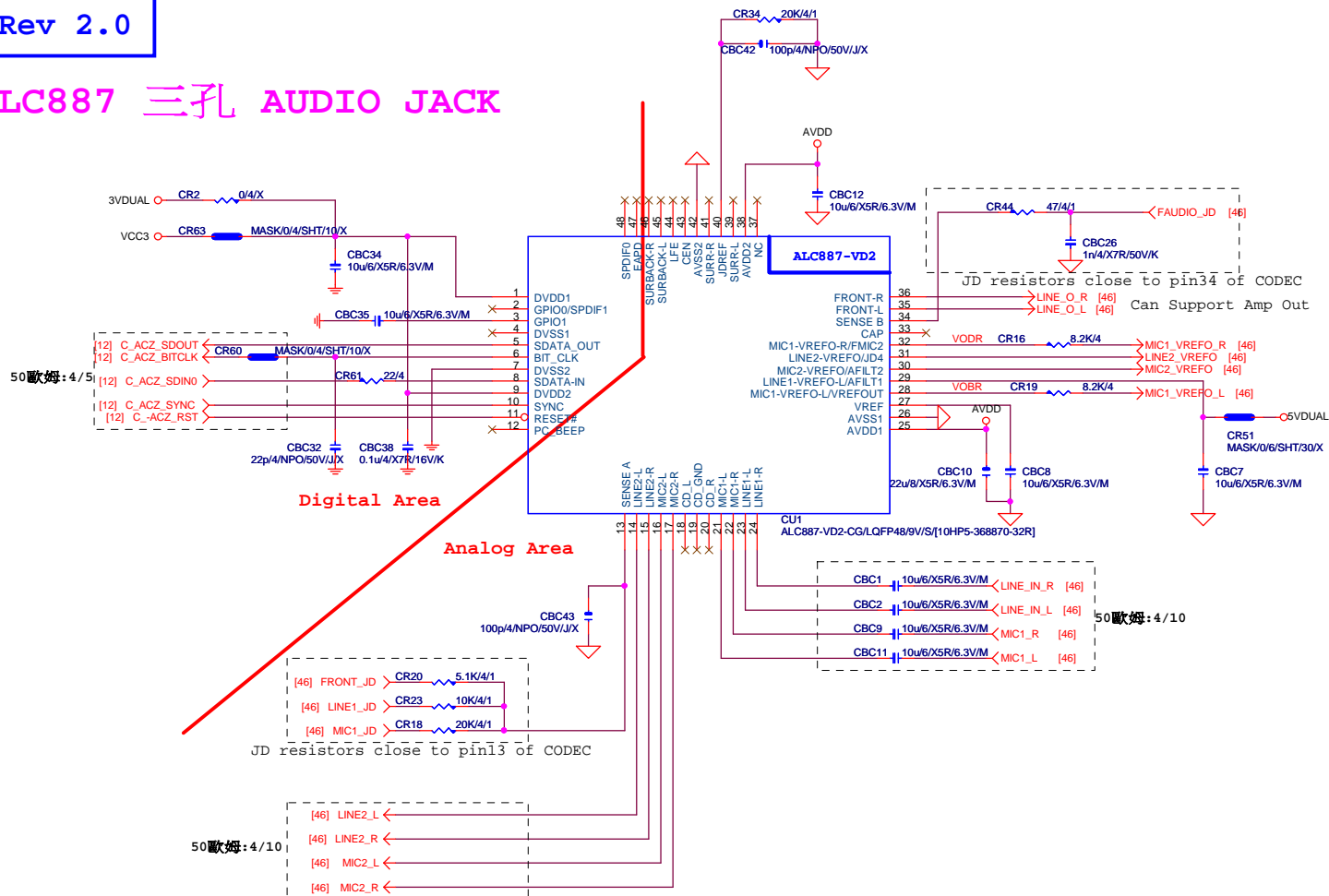
USB POWER note:可變更FUSE



LAN POWER note: lan power連接及電流



ALC887 三孔 AUDIO JACK



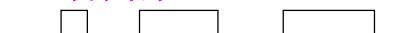
LAYOUT注意: 螺絲孔下GND方式

1. MH1空間夠, 下DGND
空間不夠, 改為Isolate
2. MH2一律改為Isolate

○ MH1	MH2 ○
DGND	Isolate

LAYOUT注意: 要加

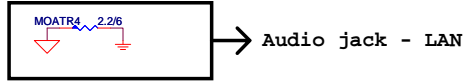
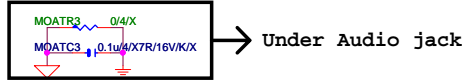
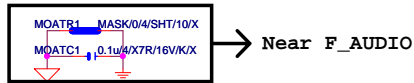
GND切割線



音效區域印刷



Rev 2.01

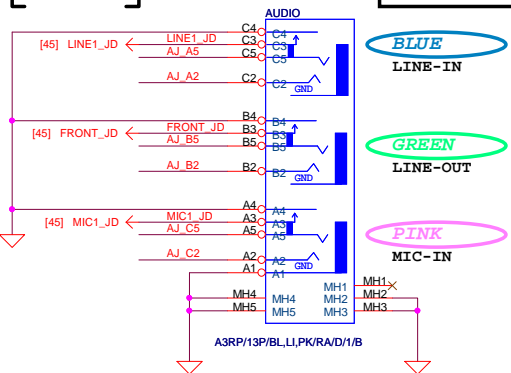
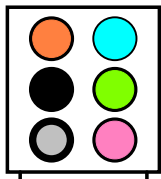


*量産前, 0ohm改short pad

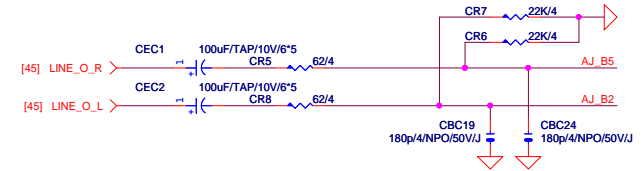
SPDIF_OUT

SPDIF_IN

AZALIA JACK



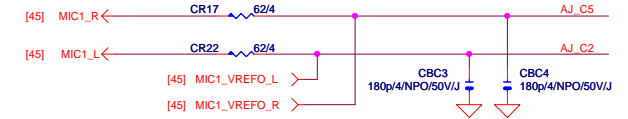
LINE-OUT



LINE-IN



MIC-IN

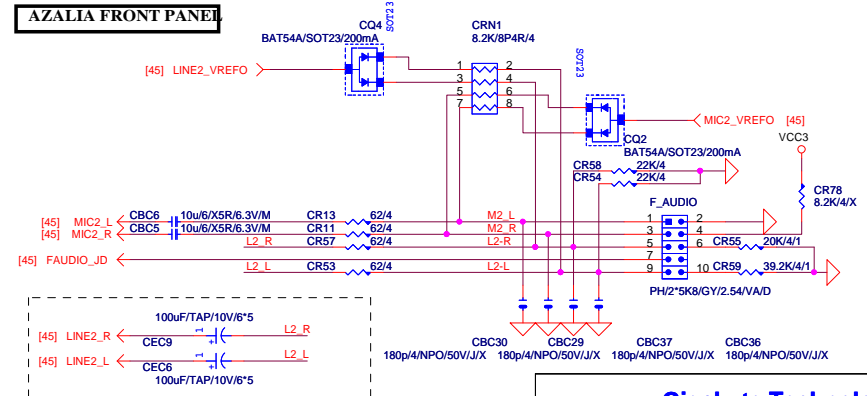


SURROUND

CEN/LFE

SURR BACK

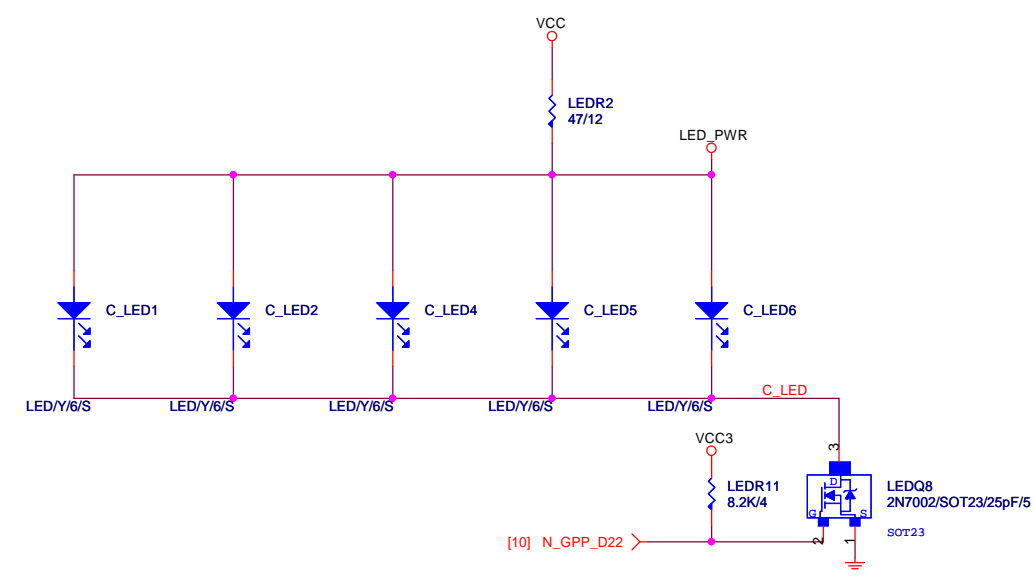
AZALIA FRONT PANEL



Gigabyte Technology

AUDIO JACK

Title	Document Number	Rev
Size Custom	B365M HD3	1.0
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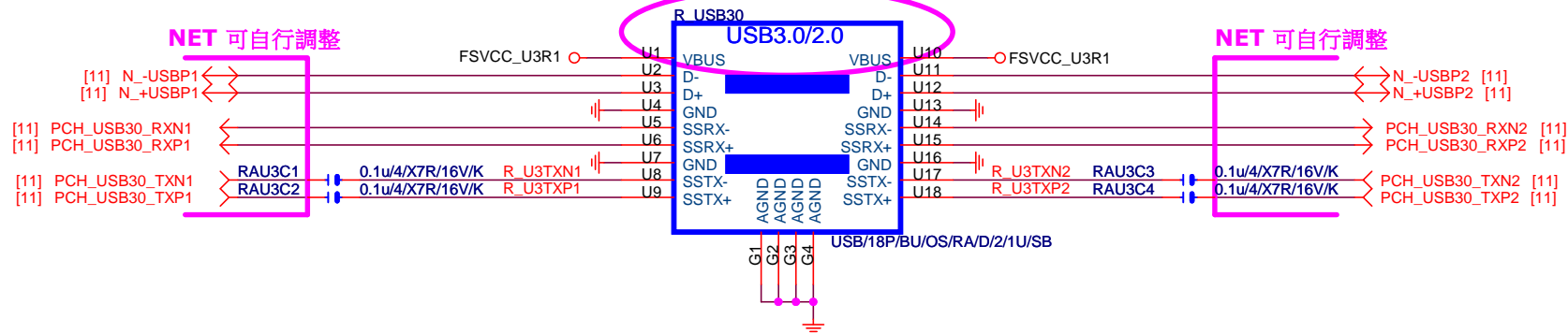


Ambient LED Control

	N_GPP_D22
Full Mode	H
OFF Mode	L

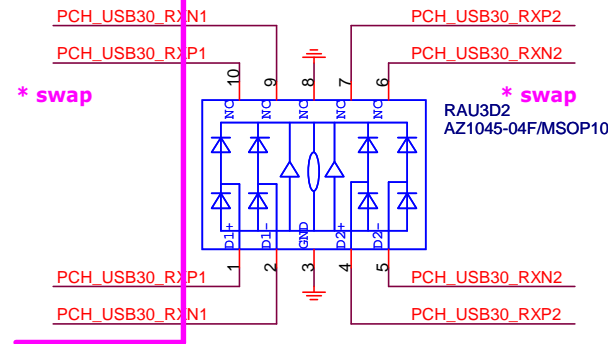
Rev: 0.7

ESD 可自行SWAP PIN ,CONN端 NET 名稱 不可

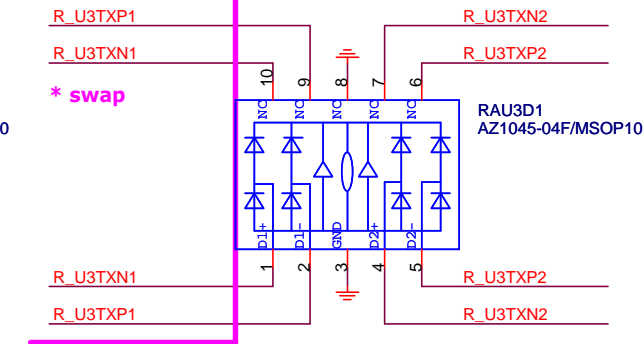


ESD

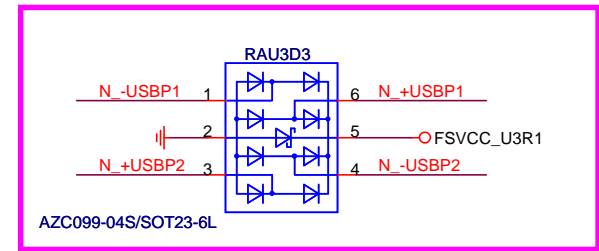
NET 可自行調整



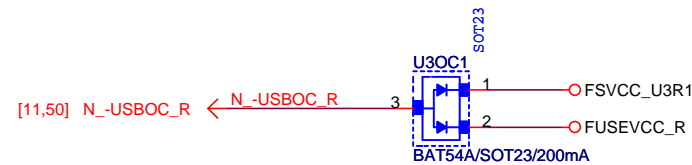
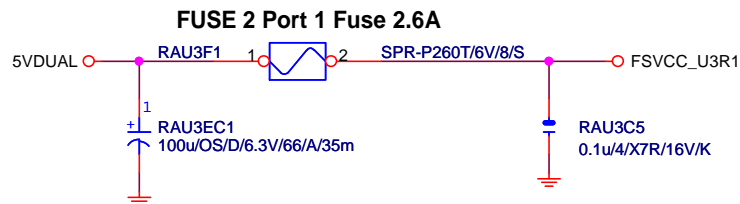
NET 可自行調整



NET 可自行調整

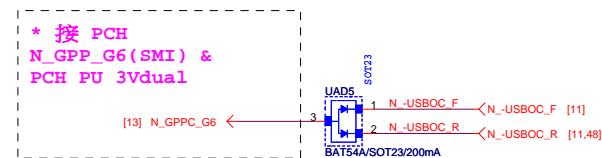
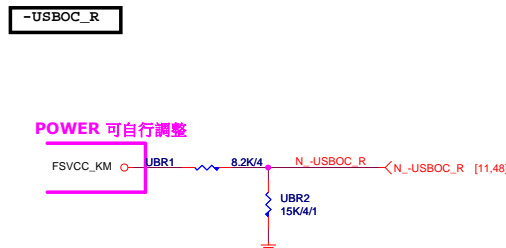
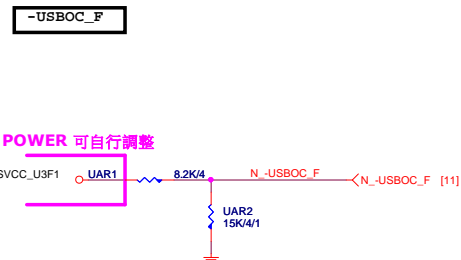
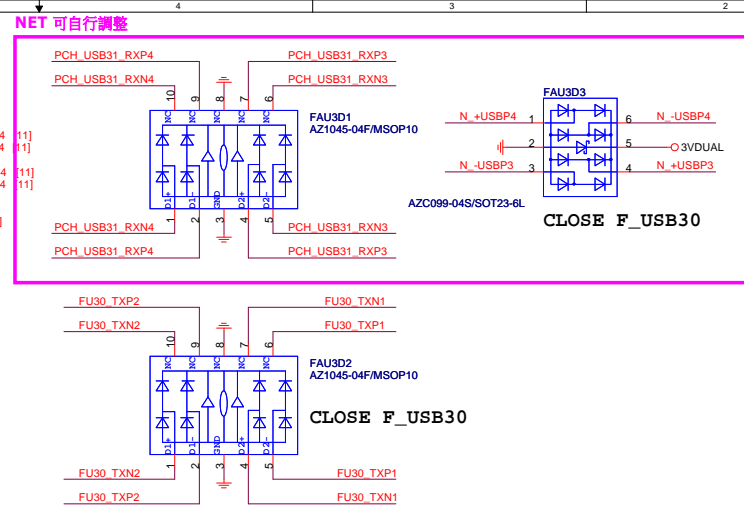
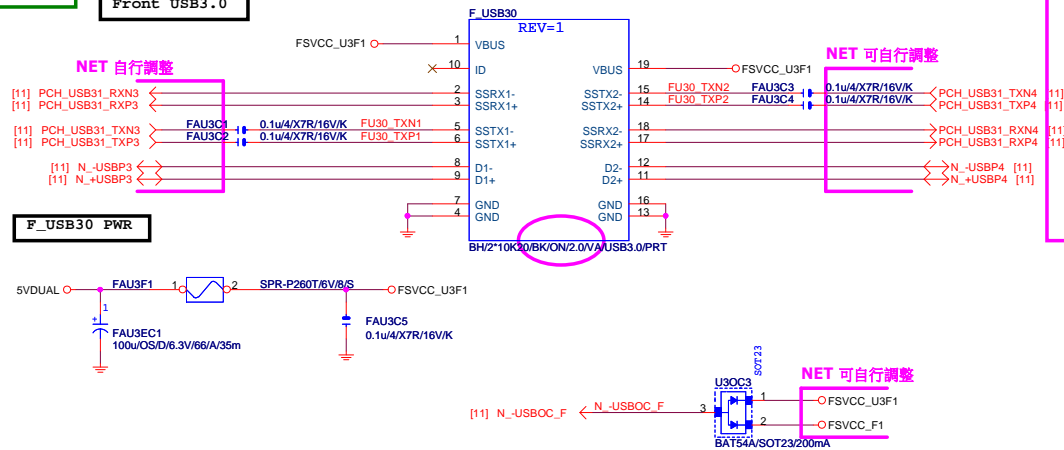


FUSE



Gigabyte Technology

Title			
R_USB30,USB_OC			
Size	Document Number	B365M HD3	
Custom			Rev 1.0
Date:	Wednesday, April 17, 2019	Sheet 48	of 56

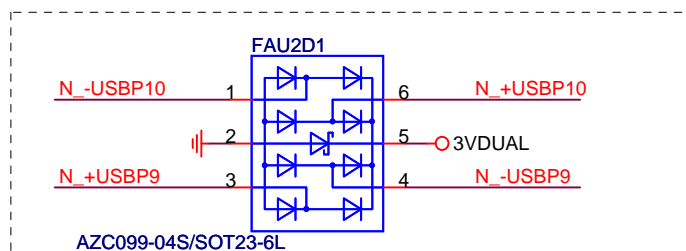
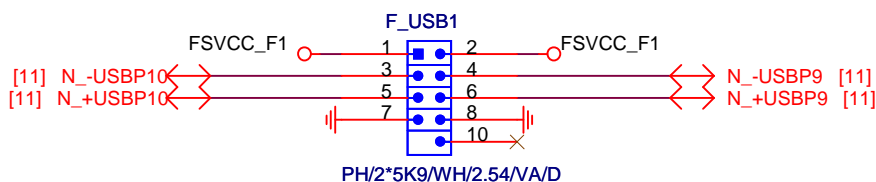


Rev: 0.7

FRONT USB1

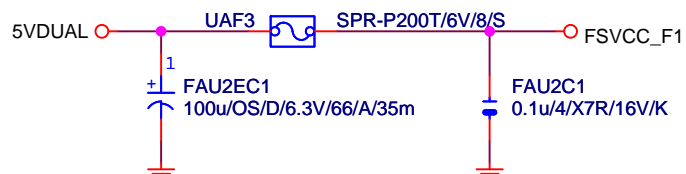
NET 可變

FUSB2X5-HS



Close to connector

FUSE 2 Port 1 Fuse 2A

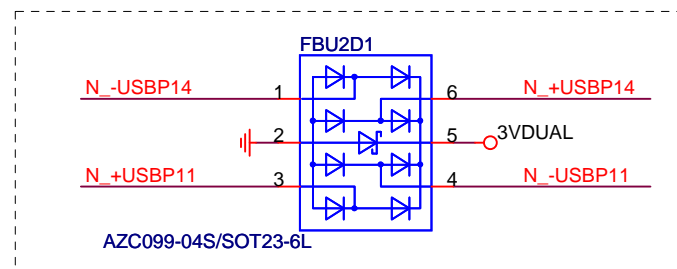
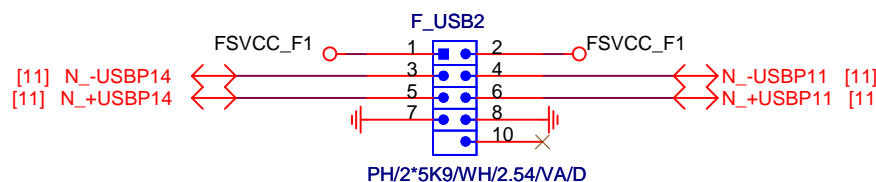


F_USB 2.0 OC SIGNAL

FRONT USB2

NET 可變

FUSB2X5-HS

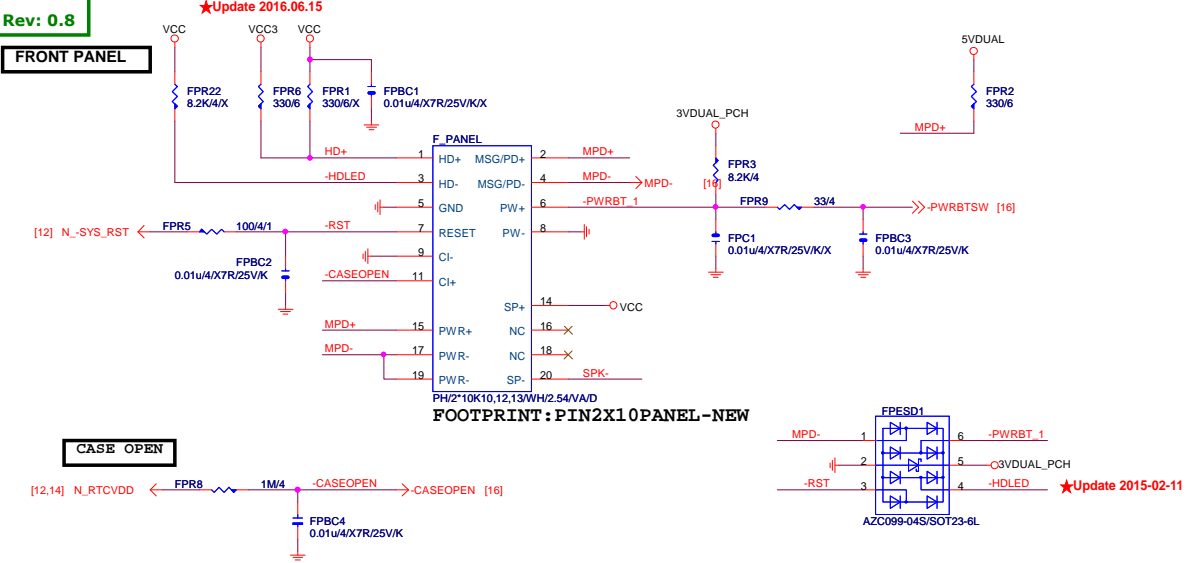


Close to connector

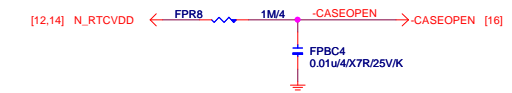
FUSE 2 Port 1 Fuse 2A

Gigabyte Technology			
Title			
USB2.0			
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	B365M HD3		1.0
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FRONT PANEL

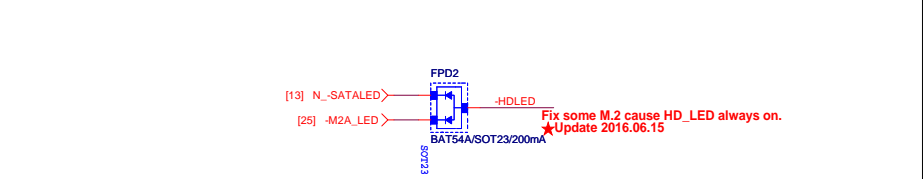


CASE OPEN

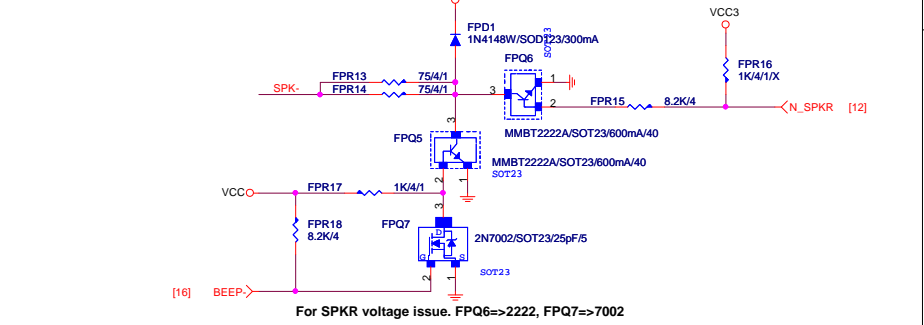


FRONT PANEL SHORT

SATA/M.2 LED

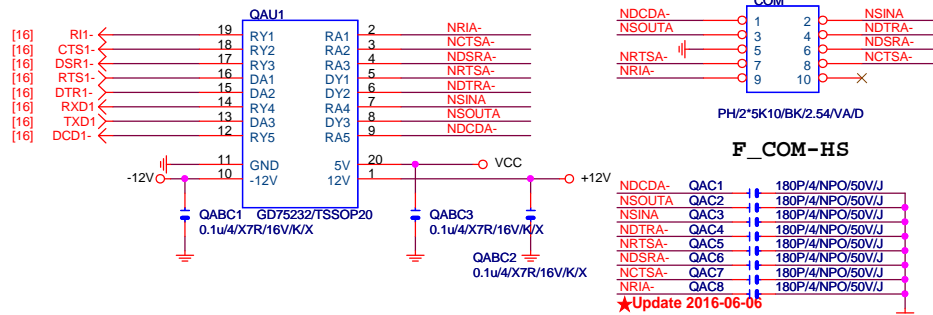


SPKR

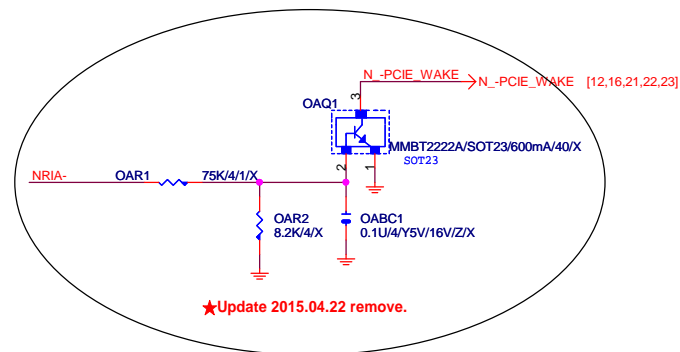


For SPKR voltage issue. FPQ6=>2222, FPQ7=>7002

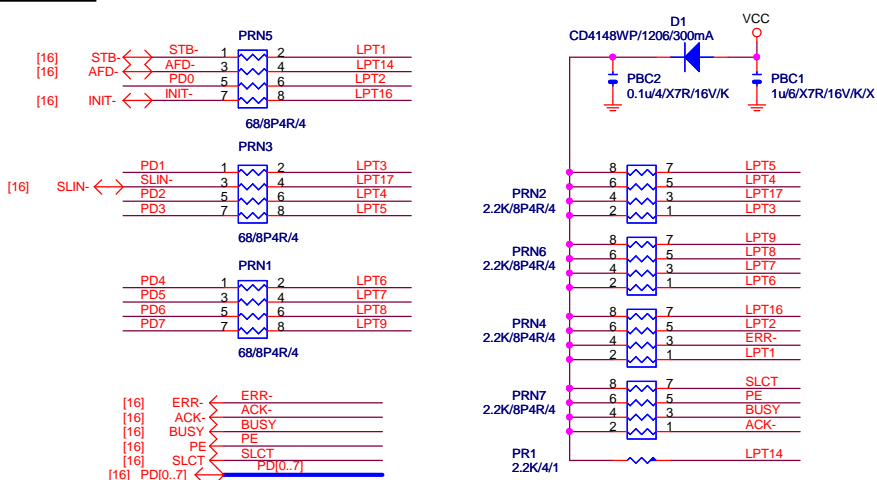
COM PORT Rev: 0.7



COM RI	N/A
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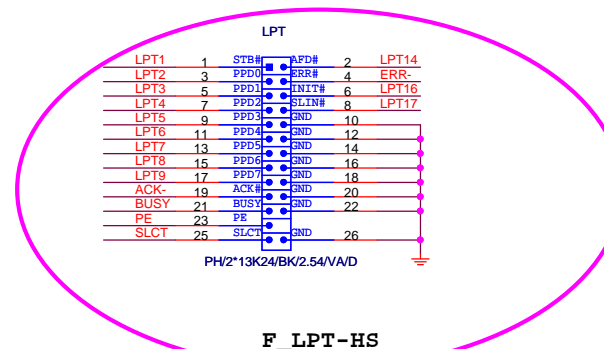


LPT PORT

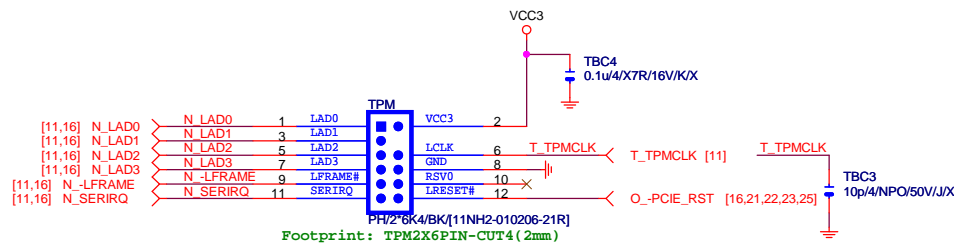


R&D技術通報151 有使用PRINT PORT的
MODEL, 需使用新料號:10HP2-118728-72R。(CHIP IT8728F/EX (GB) ITE/SMD
QFP128 PRINTPORT SORTING)料件。串電阻33 ohm改為68 ohm。

R&D技術通報151 有使用PRINT PORT的
MODEL, 需使用新料號:10HP2-118728-72R。(CHIP IT8728F/EX (GB) ITE/SMD
QFP128 PRINTPORT SORTING)料件。串電阻33 ohm改為68 ohm。

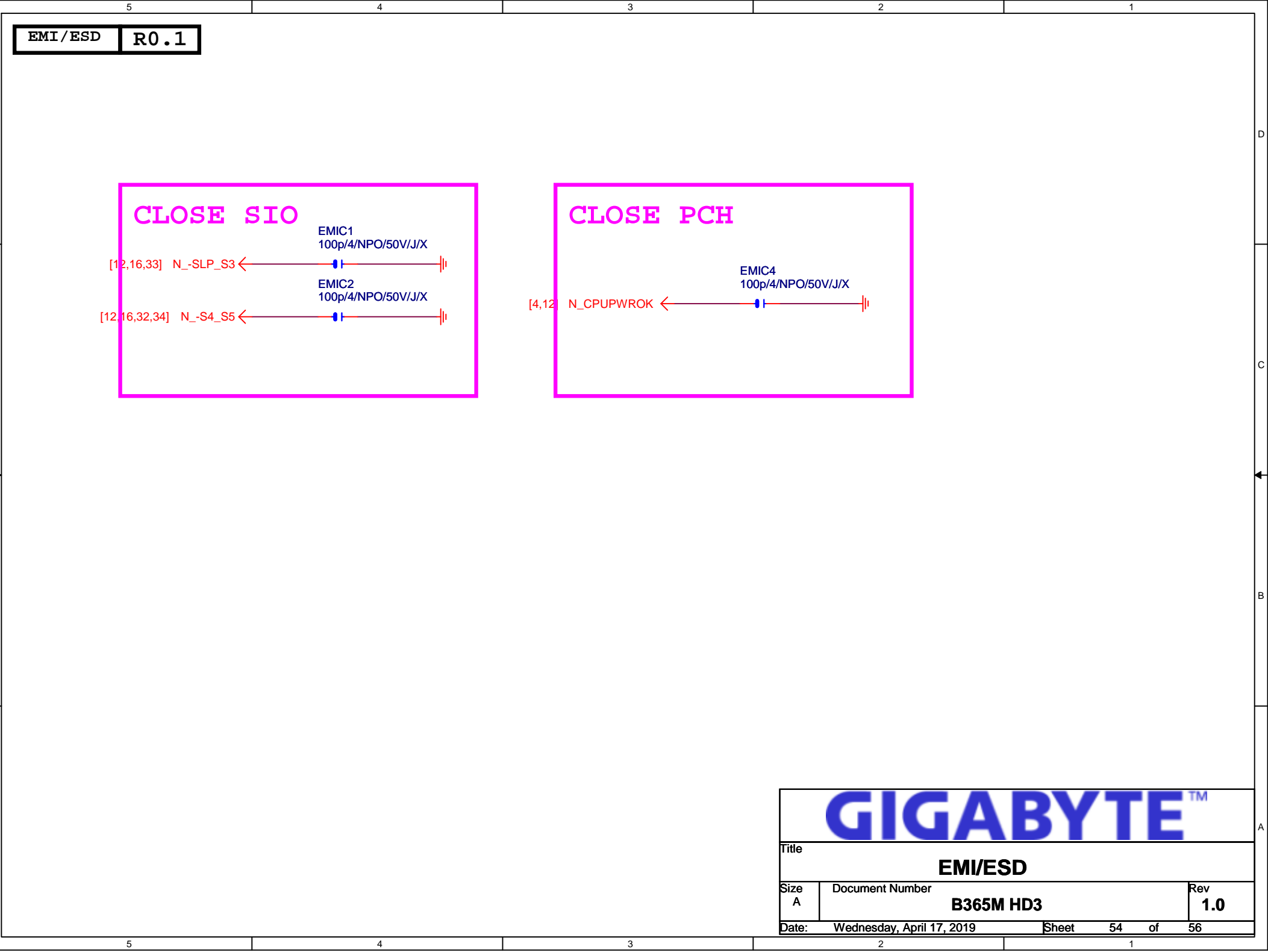


TPM CONNECT

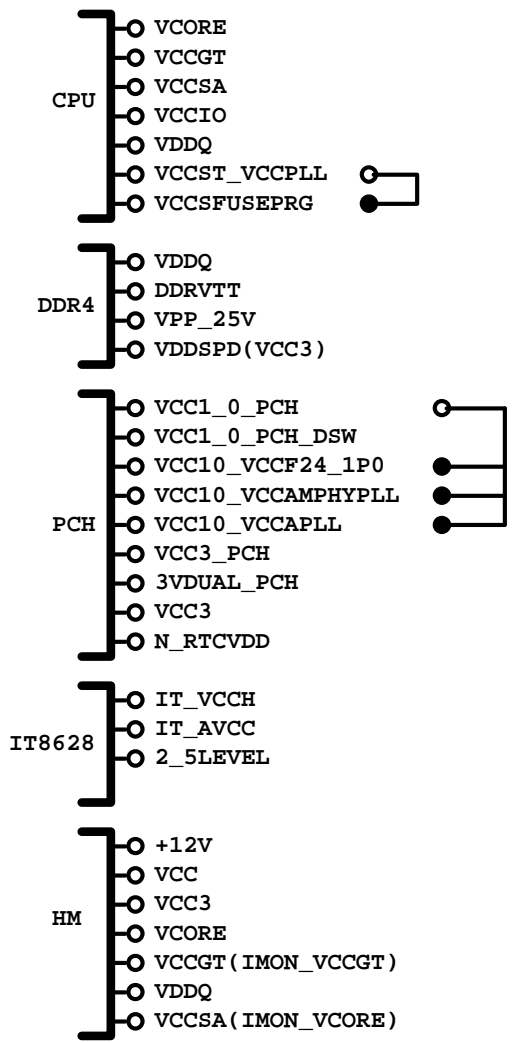


Gigabyte Technology

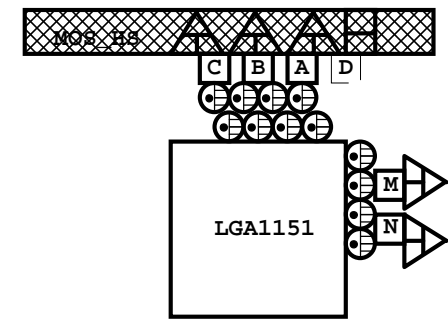
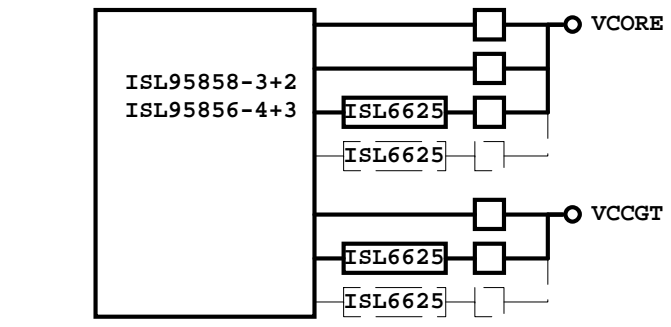
Title			
FP,F_USB,USB PWR,BZ			
Size Custom	Document Number		Rev
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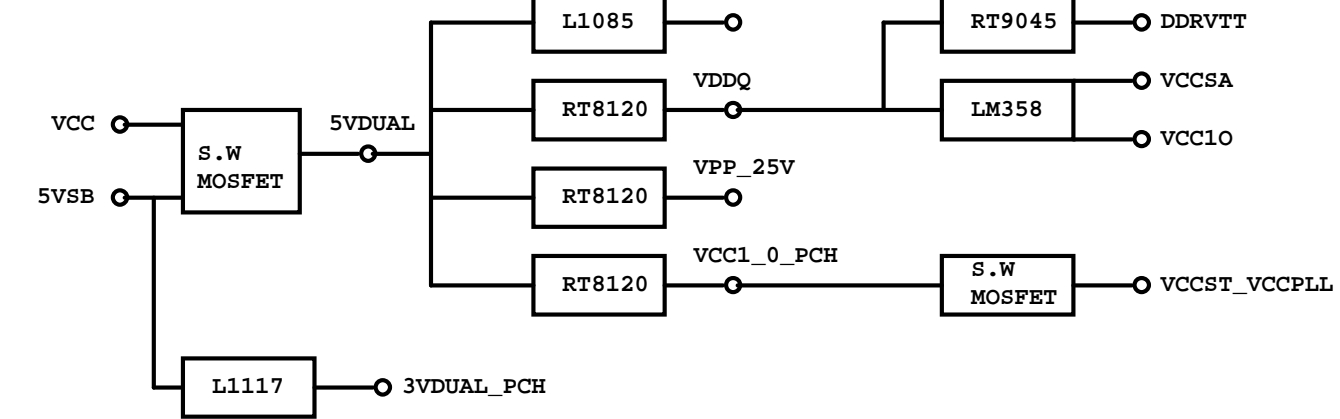
POWER BLOCK MAP



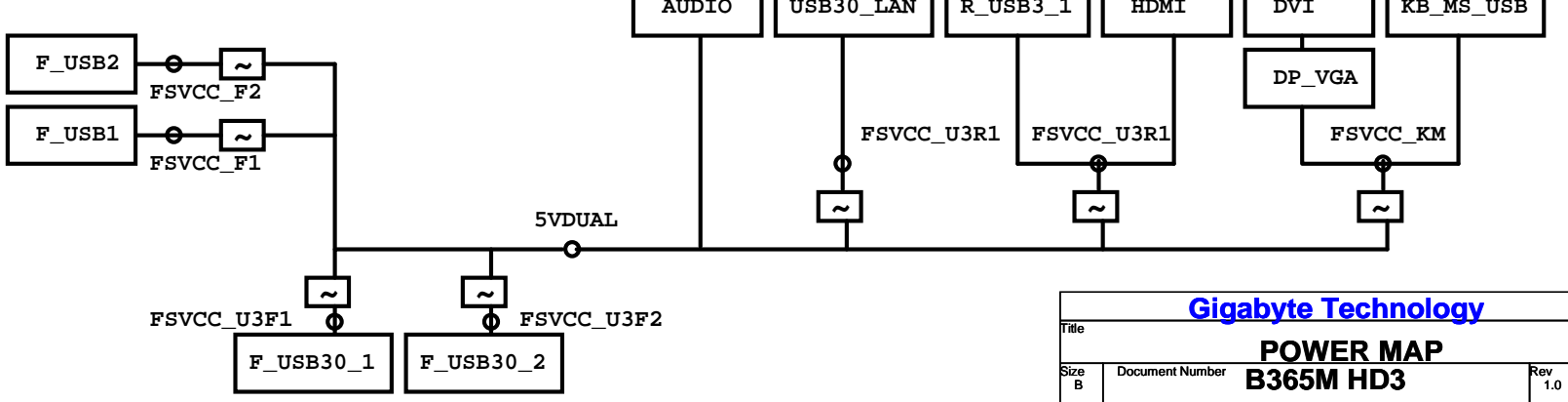
VCORE/VCCGT

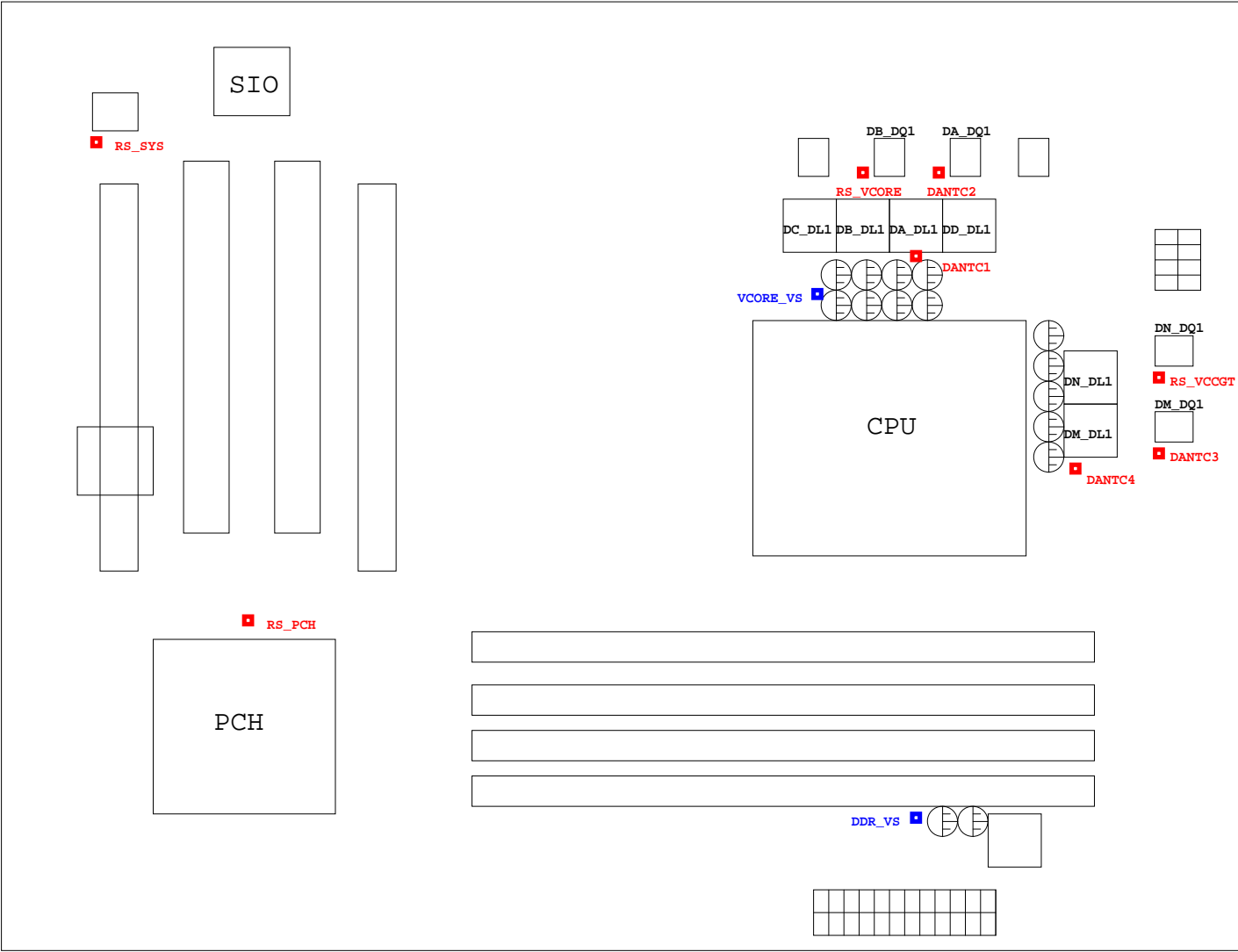


POWER



FUSE POWER F/R





熱敏電阻	擺放靠近位置	走線方式
DANTC1	DA_DL1	N/A
DANTC2	DA_DQ1	Differential
DANTC3	DM_DQ1	N/A
DANTC4	DM_DL1	Differential
RS_VCORE	DB_DQ1	N/A
RS_VCCGT	DN_DQ1	N/A
RS_PCH	PCH	N/A
RS_SYS	CU1	N/A